TEST REPORT

AS_NZS 4777.2

Grid connection of energy systems via inverters

Part 2: Inverter requirements

Report reference number .....:	**PVAU200224N005-7-R1**
Date of issue .....:	2021-03-24
Total number of pages .....:	44
Testing laboratory name.....:	**Bureau Veritas Shenzhen Co., Ltd. Dongguan Branch**
Address .....:	No. 96, Guantai Road (Houjie Section), Houjie Town, Dongguan City, Guangdong Province, 523942, People's Republic of China
Applicant's name.....:	**Shenzhen SOFAR SOLAR Co., Ltd.**
Address .....:	401, Building 4, AnTongDa Industrial Park, District 68, XingDong Community, XinAn Street, BaoAn District, Shenzhen, China.
Test specification	
Standard.....:	Short duration under voltage response test (LVRT capability) - Inverter Conformance Test Procedure For South Australia
Test report form number.....:	LVRT For South Australia VER.0
Master TRF originator .....:	Bureau Veritas Shenzhen Co., Ltd. Dongguan Branch
Matster TRF .....:	Dated 2020-08-13
Test item description .....:	**Solar Grid-tied Inverter**
Trademark .....:	SOFAR SOLAR
Model / Type.....:	SOFAR 20000TL-G2, SOFAR 25000TL-G2, SOFAR 30000TL-G2, SOFAR 33000TL-G2

Ratings ..... :	**SOFAR 20000TL-G2**	**SOFAR 25000TL-G2**	**SOFAR 30000TL-G2**	**SOFAR 33000TL-G2**
Input DC voltage range [V] ..... :	230-1100			
Full load MPPT DC voltage range [V]:	480-850	460-850	520-850	580-850
Input DC current [A] ..... :	24/24	28/28	30/30	30/30
Output AC voltage [V] ..... :	400V, 3/N/PE, 50Hz			
Output AC current [A]..... :	Max. 32	Max. 40	Max. 48	Max. 53
Output power [VA]..... :	22000	27500	33000	36300

Testing Location.....:	**Bureau Veritas Shenzhen Co., Ltd. Dongguan Branch**
Address .....:	No. 96, Guantai Road (Houjie Section), Houjie Town, Dongguan City, Guangdong Province, 523942, People's Republic of China
Tested by (name, function and signature) .....:	Lukes Lin 
Approved by (name, function and signature) .....:	James Huang 
Manufacturer's name .....:	**Shenzhen SOFAR SOLAR Co., Ltd.**
Factory address.....:	401, Building 4, AnTongDa Industrial Park, District 68, XingDong Community, XinAn Street, BaoAn District, Shenzhen, China.
Factory's name .....:	**Dongguan SOFAR SOLAR Co.,Ltd.**
Factory address .....:	1F - 6F, Building E, No. 1 JinQi Road, Bihu Industrial Park, Wulian Village, Fenggang Town, Dongguan City

Document History			
Date	**Internal reference**	**Modification / Change / Status**	**Revision**
2020-09-22	Lukes Lin	Initial report was written	0
2021-03-23	Lukes Lin	Updated test result of clause 2.2 and 2.3	R1
Supplementary information:			

Test items particulars	
Equipment mobility ..... :	Permanent connection
Operating condition ..... :	Continuous
Class of equipment..... :	Class I
Protection against ingress of water .. :	IP65 according to EN 60529
Mass of equipment [kg] ..... :	Approx. 37,0kg

Test case verdicts	
Test case does not apply to the test object ..... :	N/A
Test item does meet the requirement ..... :	P(ass)
Test item does not meet the requirement ..... :	F(ail)

Testing	
Date of receipt of test item ..... :	2020-09-11
Date(s) of performance of test ..... :	2020-09-11 to 2020-09-14, 2021-03-22

General remarks:

The test result presented in this report relate only to the object(s) tested.

The report shall state compliance of the tested objects with the requirements of Short Duration Undervoltage Disturbance Ride-Through – Inverter Conformance Test Procedure for South Australia.

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”(see Annex #)" refers to additional information appended to the report.
"(see appended table)" refers to a table appended to the report.

Throughout this report a comma is used as the decimal separator.

Copy of marking plate

SOFAR SOLAR Solar Grid-tied Inverter

Model No:	SOFAR 20000TL-G2
Max.DC Input Voltage	1100V
Operating MPPT Voltage Range	230~960V
Max. Input Current	24A/24A
Max. PV Isc	30A/30A
Nominal Grid Voltage	3/N/PE,400Vac
Max.Output Current	3x32A
Nominal Grid Frequency	50/60Hz
Nominal Output Power	20000W
Max.Output Power	22000VA
Power Factor	>0.99(adjustable+/-0.8)
Ingress Protection	IP65
Operating Temperature Range	-25℃~+60℃
Protective Class	Class I

Made in China

Manufacturer : Shenzhen SOFAR SOLAR Co.,Ltd.
Address : 401, Building 4, AnTongDa Industrial Park,
District 68, XingDong Community,XinAn Street,
BaoAn District, Shenzhen, China
VDE0126-1-1,VDE-AR-N4105,G99,IEC61727,
IEC62116,UTE C15-712-1,AS4777

SOFAR SOLAR Solar Grid-tied Inverter

Model No:	SOFAR 25000TL-G2
Max.DC Input Voltage	1100V
Operating MPPT Voltage Range	230~960V
Max. Input Current	28A/28A
Max. PV Isc	35A/35A
Nominal Grid Voltage	3/N/PE,400Vac
Max.Output Current	3x40A
Nominal Grid Frequency	50/60Hz
Nominal Output Power	25000W
Max.Output Power	27500VA
Power Factor	>0.99(adjustable+/-0.8)
Ingress Protection	IP65
Operating Temperature Range	-25℃~+60℃
Protective Class	Class I

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VDE0126-1-1,VDE-AR-N4105,G99,IEC61727,
IEC62116,UTE C15-712-1,AS4777

SOFAR SOLAR Solar Grid-tied Inverter

Model No:	SOFAR 30000TL-G2
Max.DC Input Voltage	1100V
Operating MPPT Voltage Range	230~960V
Max. Input Current	30A/30A
Max. PV Isc	37.5A/37.5A
Nominal Grid Voltage	3/N/PE,400Vac
Max.Output Current	3x48A
Nominal Grid Frequency	50/60Hz
Nominal Output Power	30000W
Max.Output Power	33000VA
Power Factor	>0.99(adjustable+/-0.8)
Ingress Protection	IP65
Operating Temperature Range	-25℃~+60℃
Protective Class	Class I

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VDE0126-1-1,VDE-AR-N4105,G99,IEC61727,
IEC62116,UTE C15-712-1,AS4777

SOFAR SOLAR Solar Grid-tied Inverter

Model No:	SOFAR 33000TL-G2
Max.DC Input Voltage	1100V
Operating MPPT Voltage Range	230~960V
Max. Input Current	30A/30A
Max. PV Isc	37.5A/37.5A
Nominal Grid Voltage	3/N/PE,400Vac
Max.Output Current	3x53A
Nominal Grid Frequency	50/60Hz
Nominal Output Power	33000W
Max.Output Power	36300VA
Power Factor	>0.99(adjustable+/-0.8)
Ingress Protection	IP65
Operating Temperature Range	-25℃~+60℃
Protective Class	Class I

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IEC62116,UTE C15-712-1,AS4777

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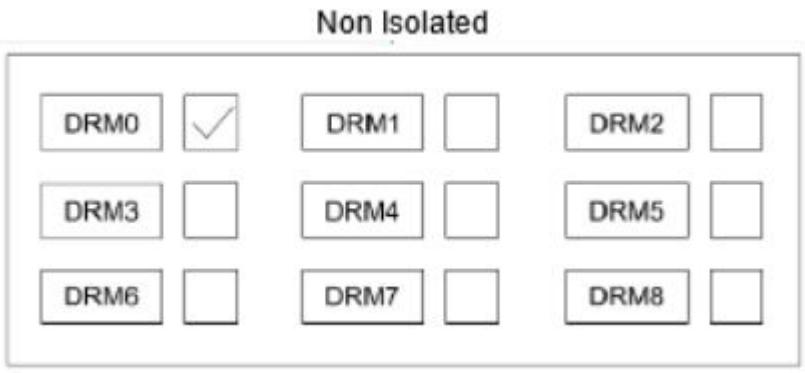

General product information:

The Solar converter converts DC voltage into AC voltage.

The DC input of Solar converter can be supplied from PV array.

The input and output are protected by Varistors to Earth. The unit is providing EMC filtering at the output toward mains. The unit does not provide galvanic separation from input to output (transformerless). The output is switched off redundant by the high power switching bridge and a two relays. This assures that the opening of the output circuit will also operate in case of one error.

Description of the electrical circuit: (Figure 1):

The internal control is redundant built. It consists of Main DSP(UC20) and slave DSP(UC73).

The Main DSP(UC20) can control the relays, measures voltage, and frequency, AC current with injected DC, insulation resistance and residual current, In addition it tests the array insulation resistance and the RCMU circuit before each start up.

The slave DSP(UC73) is using for detect residual current, also can open the relays independently and communicate with Main DSP(UC20).

The unit provides two relays in each phase. When single-fault applied to one relay, alarm an error code in display panel, another redundant relay provides basic insulation maintained between the PV array and the mains. All the relays are tested before start up. Both controllers(Main DSP(UC20), Slave DSP(UC73) can open the relays.

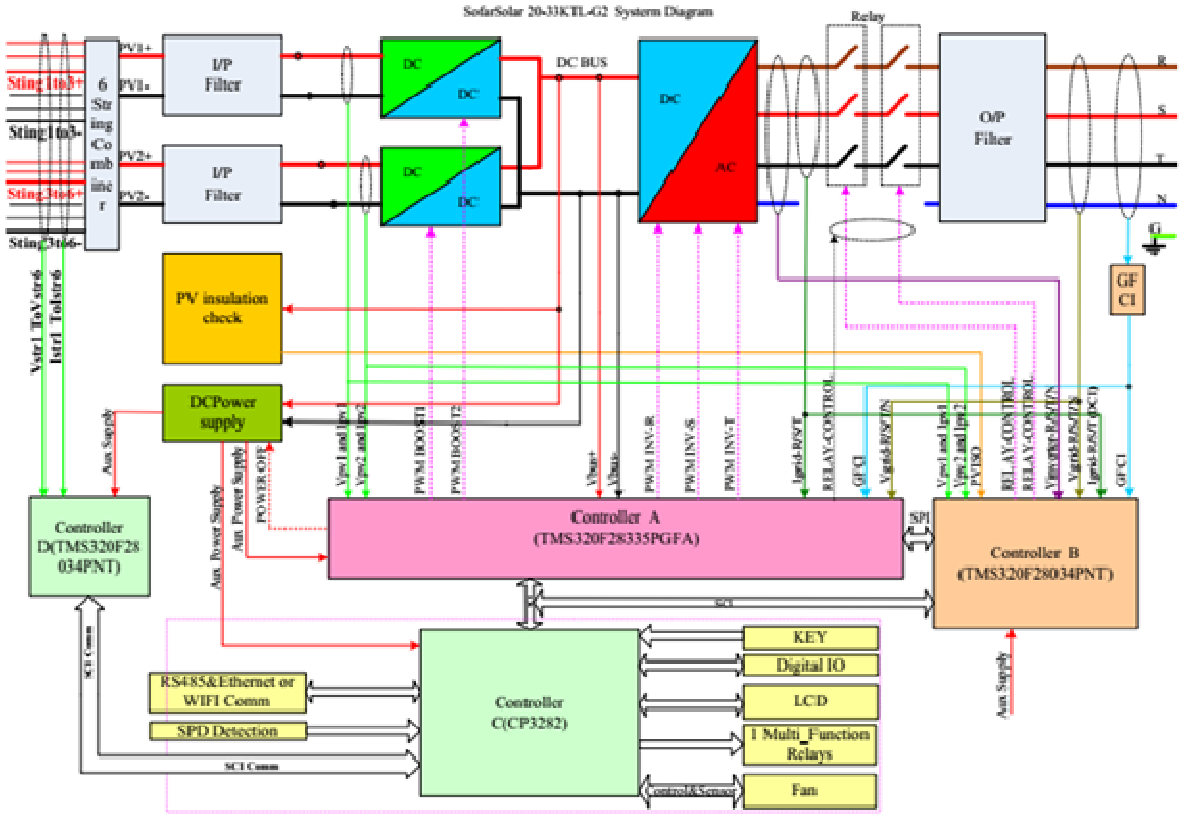

Figure 1 – Block diagram

Differences of the models in the series:

The models SOFAR 20000TL-G2, SOFAR 25000TL-G2, SOFAR 30000TL-G2 and SOFAR 33000TL-G2 are almost identical in hardware except the shown in the following table and the output power derated by software.

The difference in hardware			
Item	SOFAR 20000TL-G2	SOFAR 25000TL-G2	SOFAR 30000TL-G2 / SOFAR 33000TL-G2
Number of PV terminal	2+2	3+3	
Number of BUS capacitance	8 capacitors: 550V/110μF 2 capacitors: 1100V/40μF		10 capacitors: 550V/110μF 24 capacitors: 1100V/40μF
INV inductance	785μH	735μH	
Combiner board	Not the board	Have the board	
External fan	Not the board	2	3
Relay of output board	6pcs T9VV1K15-12S		3pcs AZSR250-2AE-12D

The product was tested on:

Hardware Version: V1.00
Software Version: V1.40

Test Results

1	General test and reporting requirments	
Clause	**Requirement – Test**	**Verdict**
1.1	General	**P**
1.2	Test condition	**P**
1.3	Inverter setup	**P**
1.4	Grid source	**P**
2	**Test procedure**	
2.1	General	**P**
2.2	Undervoltage (V<) disconnection test in response to event duration exceeding trip delay time	**P**
2.3	Undervoltage (V<) withstand test in response to event duration of less than trip delay time	**P**

2.2 Under voltage (V<) trip setting of disconnection test in response to event duration exceeding trip delay time	P

L1 phase

	Output Current level: 50+/-5% of rated current						
Test	**Voltage (V)**			**Time to disconnect (s) (Trip delay 1s)**			**Time to reconnection (s)**
Limit	**--**			**<=2s**			**>=60s**
Grid source voltage 230V down to 177,5 V (2,.5 V below 180 V)	177,5			2,0			--
Measured value	177,6	177,6	177,6	1,230	1,220	1,240	--
Return the voltage (177.5 V) to the grid test voltage (230V)	230			--			--
Measured value	230,1			--			62,0

L2 phase

	Output Current level: 50+/-5% of rated current						
Test	**Voltage (V)**			**Time to disconnect (s) (Trip delay 1s)**			**Time to reconnection (s)**
Limit	--			<=2s			>=60s
Grid source voltage 230V down to 177,5 V (2,.5 V below 180 V)	177,5			2,0			--
Measured value	177,7	177,6	177,7	1,230	1,240	1,240	--
Return the voltage (177.5 V) to the grid test voltage (230V)	230			--			--
Measured value	230,1			--			62,0

L3 phase

	Output Current level: 50+/-5% of rated current						
Test	**Voltage (V)**			**Time to disconnect (s) (Trip delay 1s)**			**Time to reconnection (s)**
Limit	--			<=2s			>=60s
Grid source voltage 230V down to 177,5 V (2,.5 V below 180 V)	177,5			2,0			--
Measured value	177,6	177,3	177,4	1,240	1,230	1,230	--
Return the voltage (177.5 V) to the grid test voltage (230V)	230			--			--
Measured value	230,1			--			62,0

All phases

	Output Current level: 50+/-5% of rated current		
Test	**Voltage (V)**	**Time to disconnect (s) (Trip delay 1s)**	**Time to reconnection (s)**

2.2 Under voltage (V<) trip setting of disconnection test in response to event duration exceeding trip delay time	**P**

Limit	--			<=2s			>=60s	
Grid source voltage 230V down to 177,5 V (2,.5 V below 180 V)	177,5			2,0			--	
Measured value	177,8	177,6	177,7	1,520	1,525	1,510	--	
Return the voltage (177.5 V) to the grid test voltage (230V)	230			--			--	
Measured value	230,2			--			62,0	

Test procedure:
The disconnection time for the protective function undervoltage (180 V) for a voltage step shall be confirmed. The procedure shall be as follows:
(a) Set the grid source equal to the grid test voltage. The energy source shall be varied until the a.c. output of the device under test equals 50 ± 5 % of its rated current output.
NOTE: For three-phase inverters or inverter combinations, the required inverter output is based on the per phase inverter current rating.
(b) The grid source voltage shall be stepped to 177.5 V (2.5 V below 180 V) with the step change completed within 2 ms and occurring at the zero crossing of the grid source voltage. The time interval between the start of the voltage step and the device under test disconnecting from the grid source shall be recorded.
(c) Adjust the grid source to return the voltage to the grid test voltage. The reconnection time (the time taken for the device under test to reconnect to the grid source) shall be recorded.

Note:

The Voltage required to trip is the setting 177.5V (180V minus 2.5V). The time delay can be measured at a larger deviation than the minimum required to operate the protection. It has to be in the range of *+/- 2,3V* of the grid test voltage.

Diagram of under-voltage protection: L1 phase

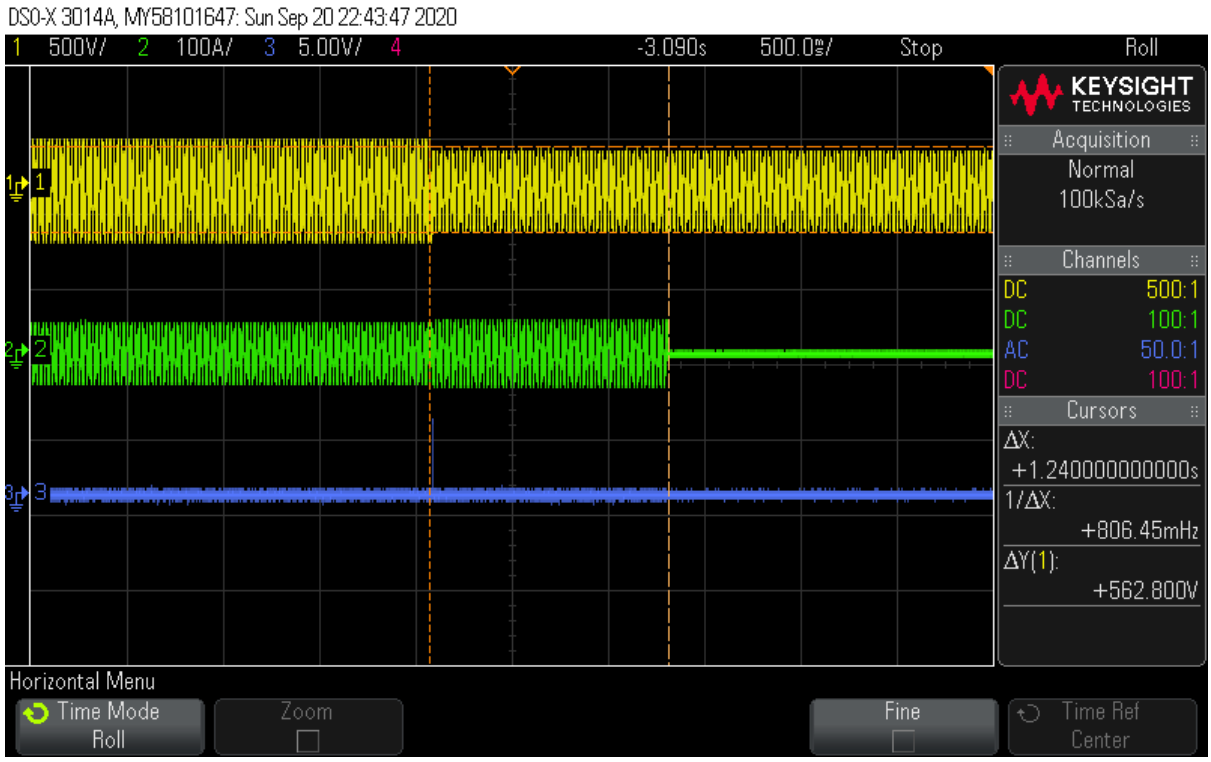

Diagram of under-voltage protection: L2 phase

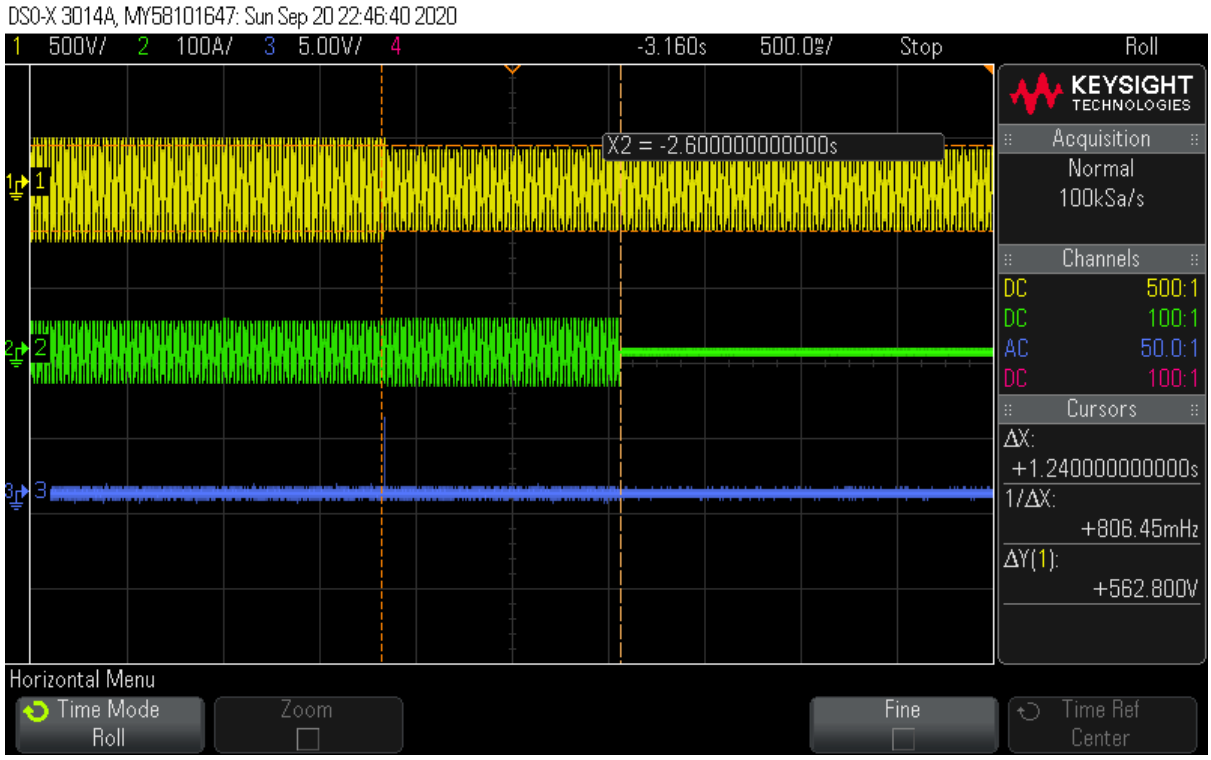

Diagram of under-voltage protection: L3 phase

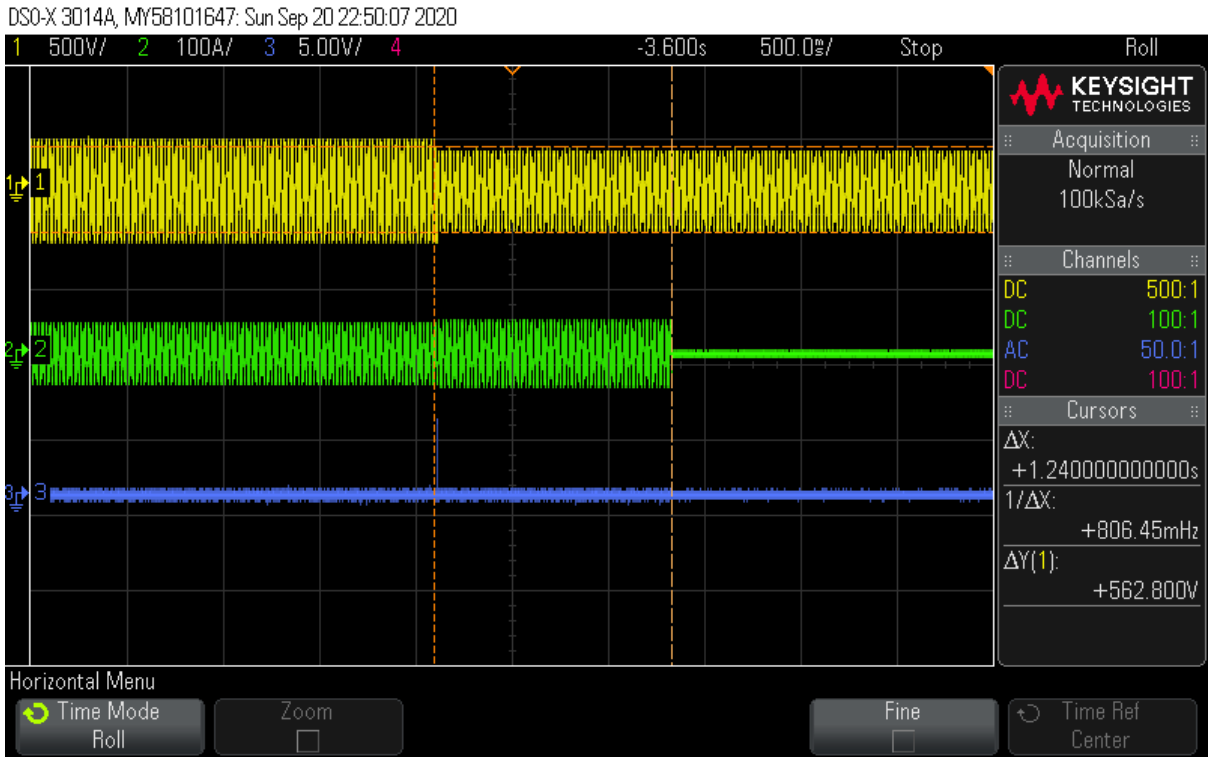

Diagram of under-voltage protection: All phase

2.3 Undervoltage (V<) withstand test in response to event duration of less than trip delay time	

Test procedure

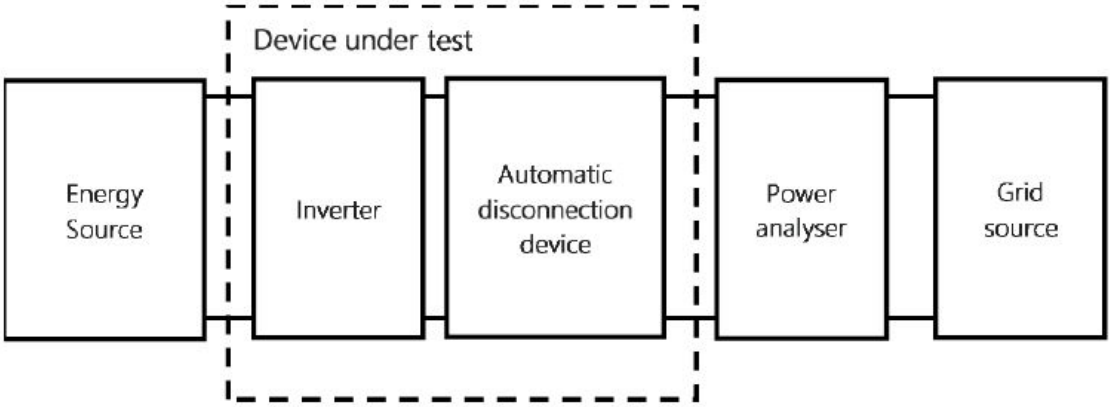

Figure 1 – Test circuit of voltage limits

The trip delay requirement for the protective function undervoltage 1 (V <) of 180 V for a voltage step shall be confirmed. The procedure shall be as follows:

(d) Set the grid source equal to the grid test voltage. Vary the energy source until the a.c. output of the device under test equals 50 ± 5 % of its rated current output.
NOTE: For three-phase inverters or inverter combinations, the required inverter output is based on the per phase inverter current rating.

(e) Record the stabilised active power output.

(f) Step the grid source voltage down to 50 V with the step change completed within 2 ms and occurring at the zero crossing of the grid source voltage, remain at 50 V for 220 ms. Increase the grid source voltage to the grid test voltage with the step change completed within 2 ms and occurring at the zero crossing of the grid source voltage. Record the time interval between each voltage step passing through 180 V (i.e. the duration for which voltage lies below 180 V).

NOTE: For three phase systems, the test shall be conducted at the zero-crossing for each phase individually, and additionally for all three phases stepped together at the zero-crossing for one of the phases.

(g) After 1 second, record the active power output, and confirm it is equal to that recorded at Step (e) ± 4 %.
NOTE: There is no defined behaviour of the inverter during the simulated fault. Monitor and recording at this stage is to better understand the anticipated inverter response.

List of tests	Residual amplitude of phase-to-neutral voltage V	Tolerance	Duration [ms]	Form (*)
Type for single phase inverter				
test 1 one-phase symmetrical fault	50V	± 0,01Un	220	
Type for three phase inverter				
test 1 a) one-phase: L1 symmetrical fault	50V	± 0,01Un	220	
test 1 b) one-phase: L1 symmetrical fault	50V	± 0,01Un	220	

test 1 c) one-phase: L1 symmetrical fault	50V	± 0,01Un	220	
test 2 a) one-phase: L2 symmetrical fault	50V	± 0,01Un	220	
test 2 b) one-phase: L2 symmetrical fault	50V	± 0,01Un	220	
test 2 c) one-phase: L2 symmetrical fault	50V	± 0,01Un	220	
test 3 a) one-phase: L3 symmetrical fault	50V	± 0,01Un	220	
test 3 b) one-phase: L3 symmetrical fault	50V	± 0,01Un	220	
test 3 c) one-phase: L3 symmetrical fault	50V	± 0,01Un	220	
test 4 a) all-phase: L1, L2, L3 symmetrical fault	50V	± 0,01Un	220	
test 4 b) all-phase: L1, L2, L3 symmetrical fault	50V	± 0,01Un	220	
test 4 c) all-phase: L1, L2, L3 symmetrical fault	50V	± 0,01Un	220	

Criteria for acceptance

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a. The device under test shall remain connected for the duration of test step (f).

b. At Step (g) the device under test shall have recovered its active power output to that recorded at Step (e) ± 4 % within 1 second.

Graph of LVRT testing

List of tests	Output Current level: 50+/-5% of rated current			
	Residual amplitude of phase-to-phase voltage (V)	**Duration limit of Voltage dips [ms]**	**Measured duration [ms]**	**Measured power recover times (ms)**
L1 phase				
Test 1 a) – one-phase symmetrical fault	50	220	231	140
Test 1 b) – one-phase symmetrical fault	50	220	230	139
Test 1 c) one-phase symmetrical fault	50	220	228	145
Test voltage	Voltage 230V +/- 1%			
Before test - Active power output (W)	16,506	16,497	16,500	
After test - Active power output(W) after 1s	16,499	16,488	16,500	
Limit(%)	+/- 4 %	+/- 4 %	+/- 4 %	
L2 phase				
Test 2 a) – one-phase symmetrical fault	50	220	230	145
Test 2 b) – one-phase symmetrical fault	50	220	229	144
Test 2 c) one-phase symmetrical fault	50	220	229	143
Test voltage	Voltage 230V +/- 1%			
Before test - Active power output (W)	16,503	16,506	16,501	
After test - Active power output(W) after 1s	16,510	16,506	16,510	
Limit(%)	+/- 4 %	+/- 4 %	+/- 4 %	
L3 phase				
Test 3 a) – one-phase symmetrical fault	50	220	230	137
Test 3 b) – one-phase symmetrical fault	50	220	230	139
Test 3 c) one-phase symmetrical fault	50	220	229	136
Test voltage	Voltage 230V +/- 1%			
Before test - Active power output (W)	16,507	16,497	16,493	
After test - Active power output(W) after 1s	16,509	16,500	16,503	
Limit(%)	+/- 4 %	+/- 4 %	+/- 4 %	

Graph of LVRT testing

All (L1,L2,L3) phase				
Test 4 a) – All-phase symmetrical fault	50	220	226	140
Test 4 a) – All-phase symmetrical fault	50	220	224	144
Test 4 a) – All-phase symmetrical fault	50	220	224	140

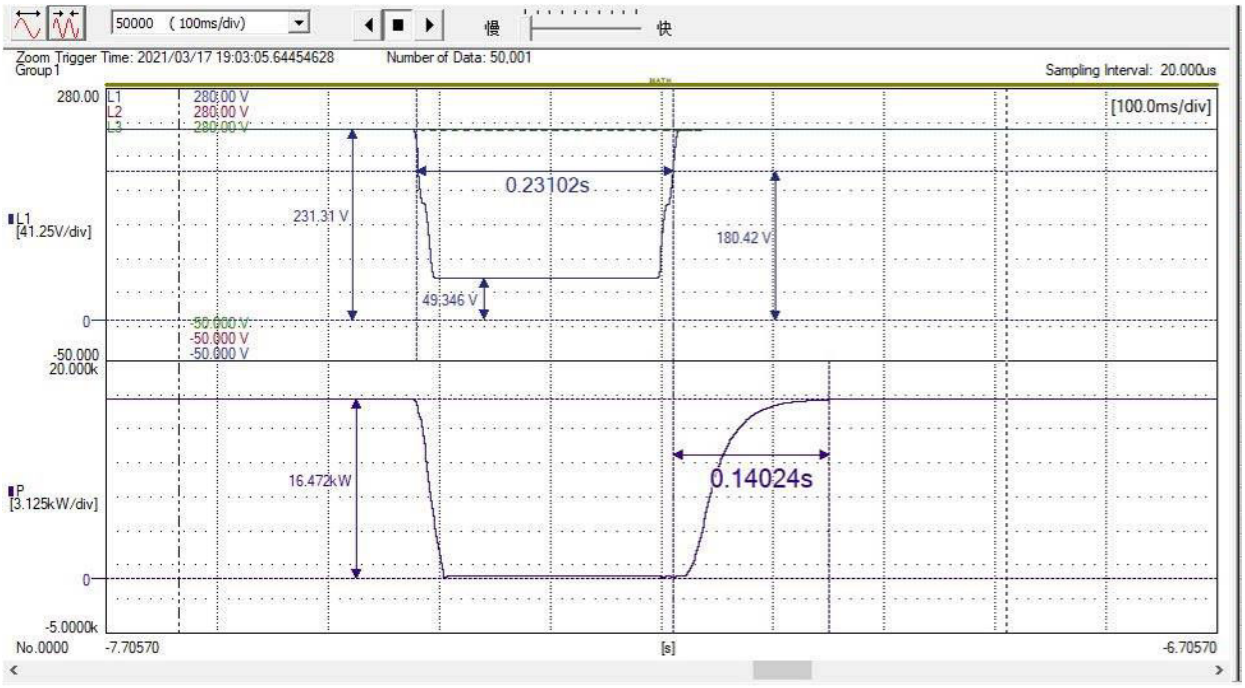
Test voltage	Voltage 230V +/- 1%		
Before test - Active power output (W)	16,547	16,472	16,472
After test - Active power output(W) after 1s	16,579	16,468	16,477
Limit(%)	+/- 4 %	+/- 4 %	+/- 4 %

Test conditions:
Voltage simulator fall and rise time: < 2ms

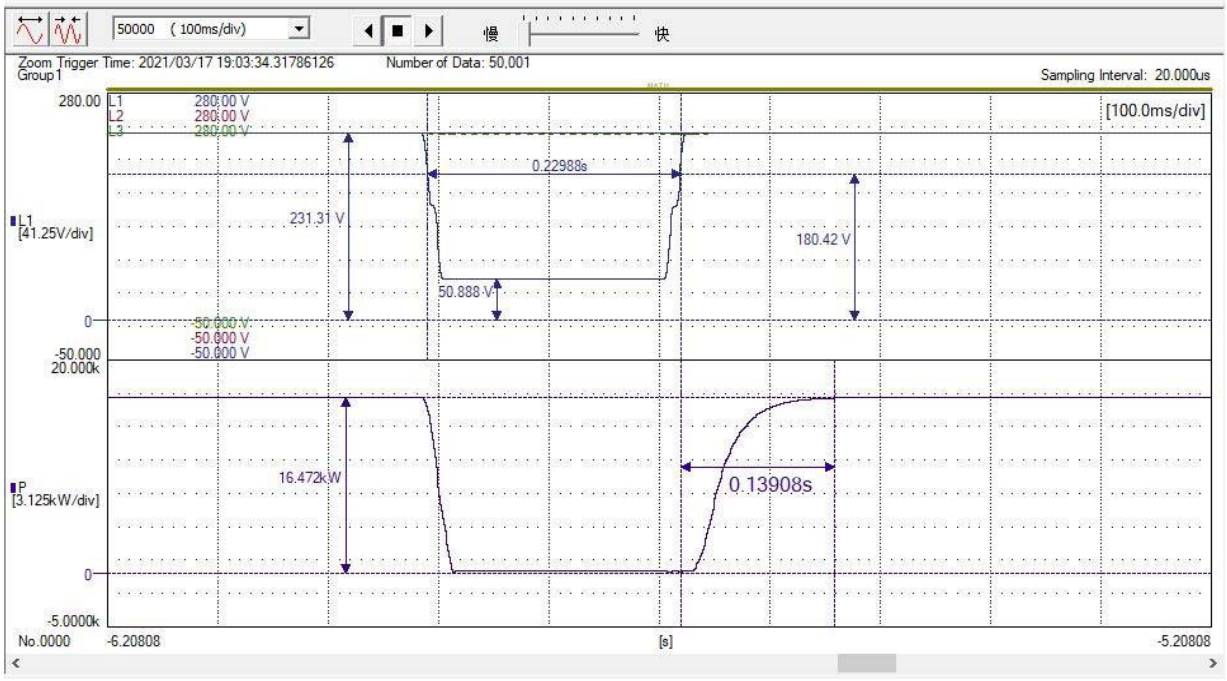
The test conditions are performed as 50% ± 5% of In conditions. The inverter feeds maximal active and reactive power during the complete test.

Note:

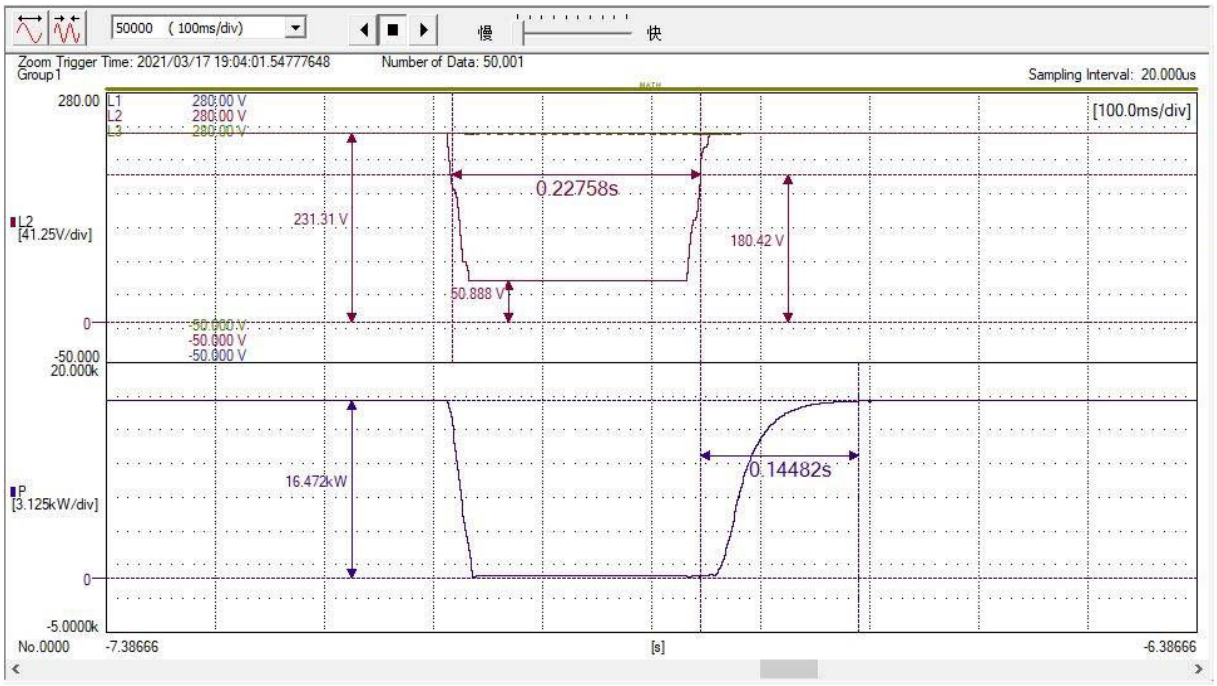
Test 1 a) – one-phase symmetrical fault



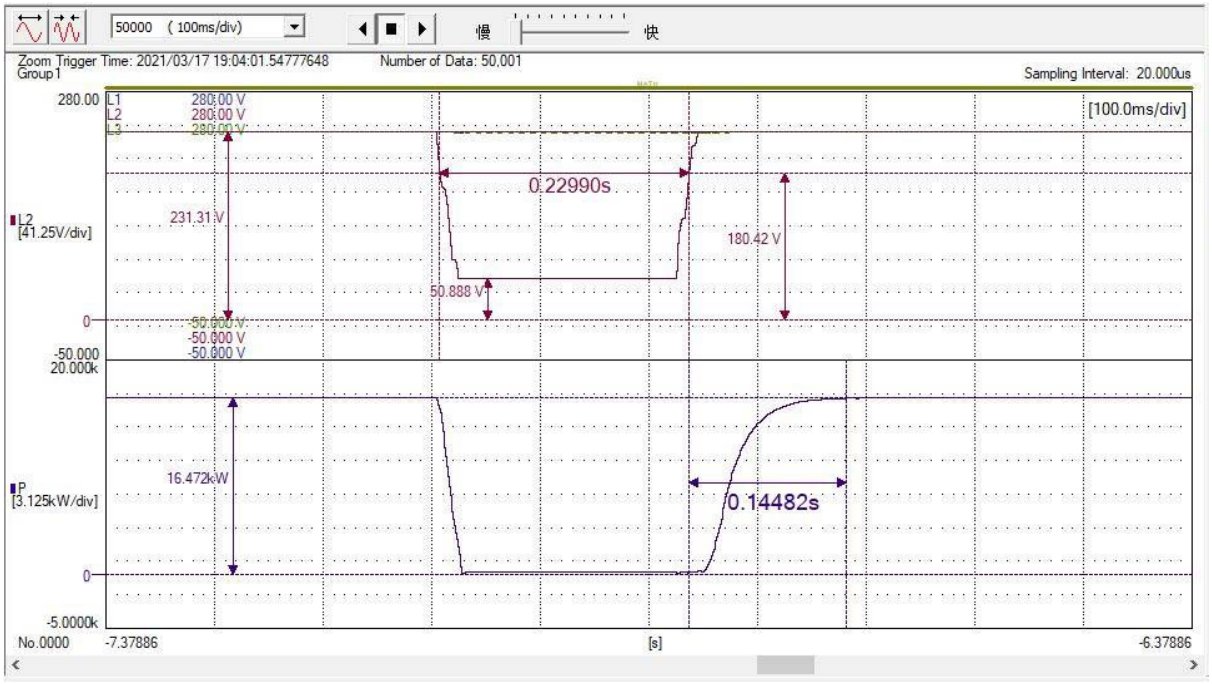
Test 1 b) – one-phase symmetrical fault



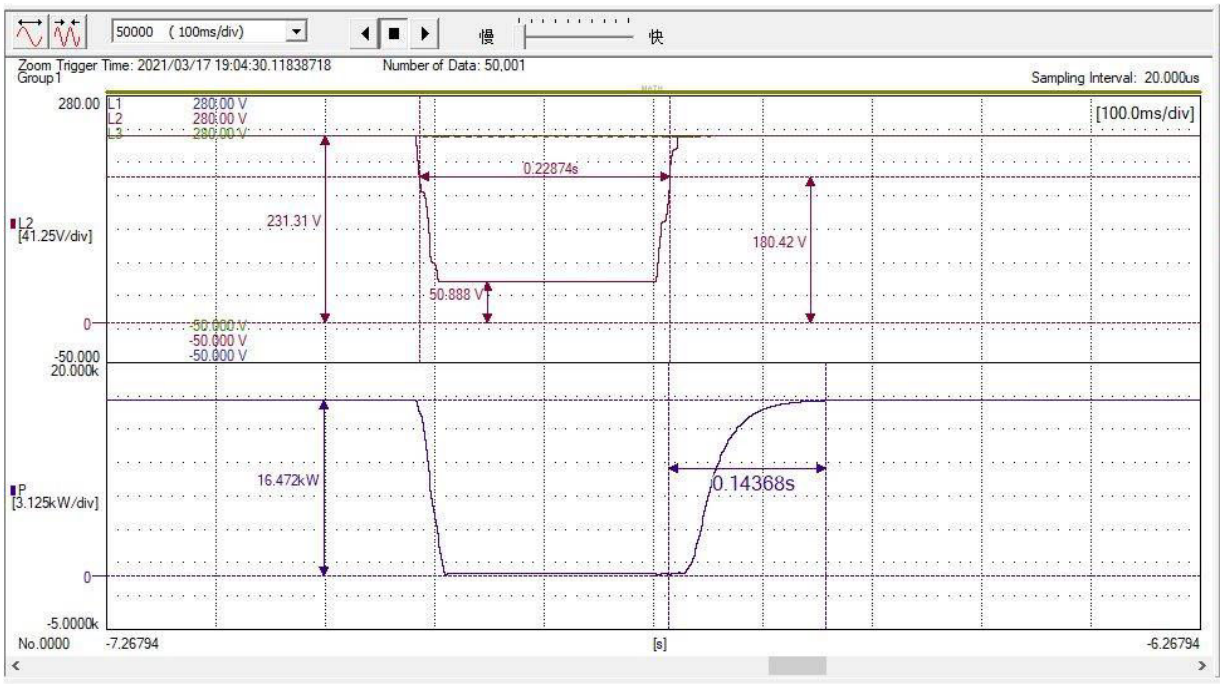
Test 1 c) – one-phase symmetrical fault



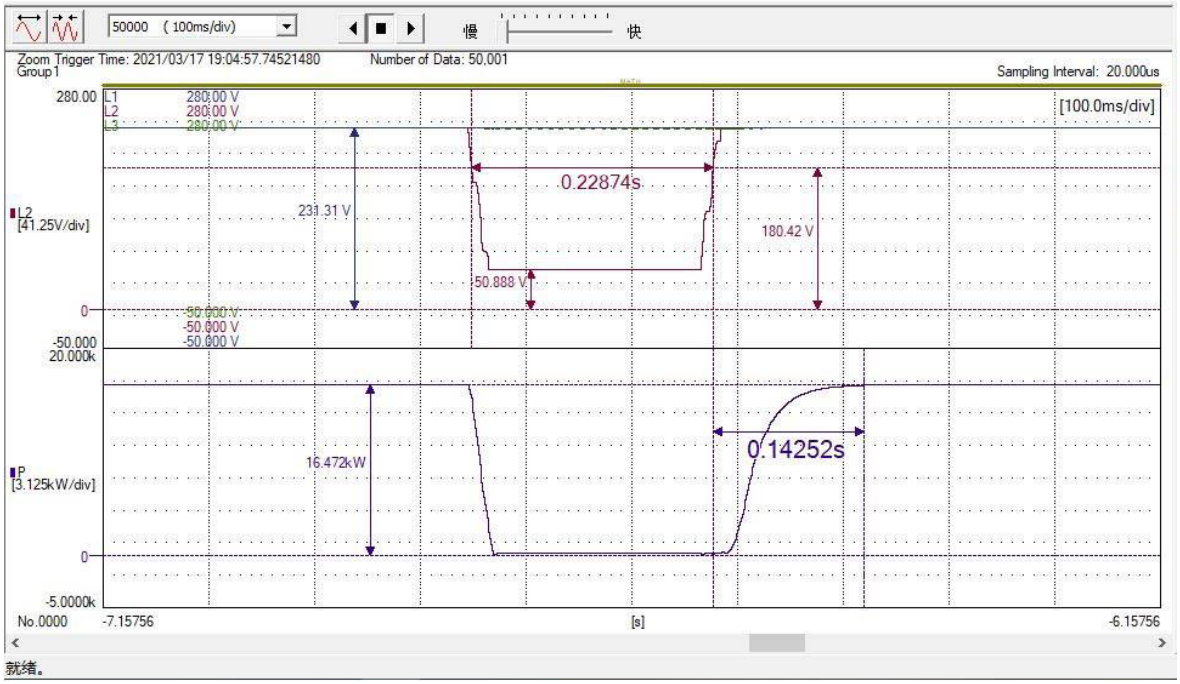
Test 2 a) – one-phase symmetrical fault



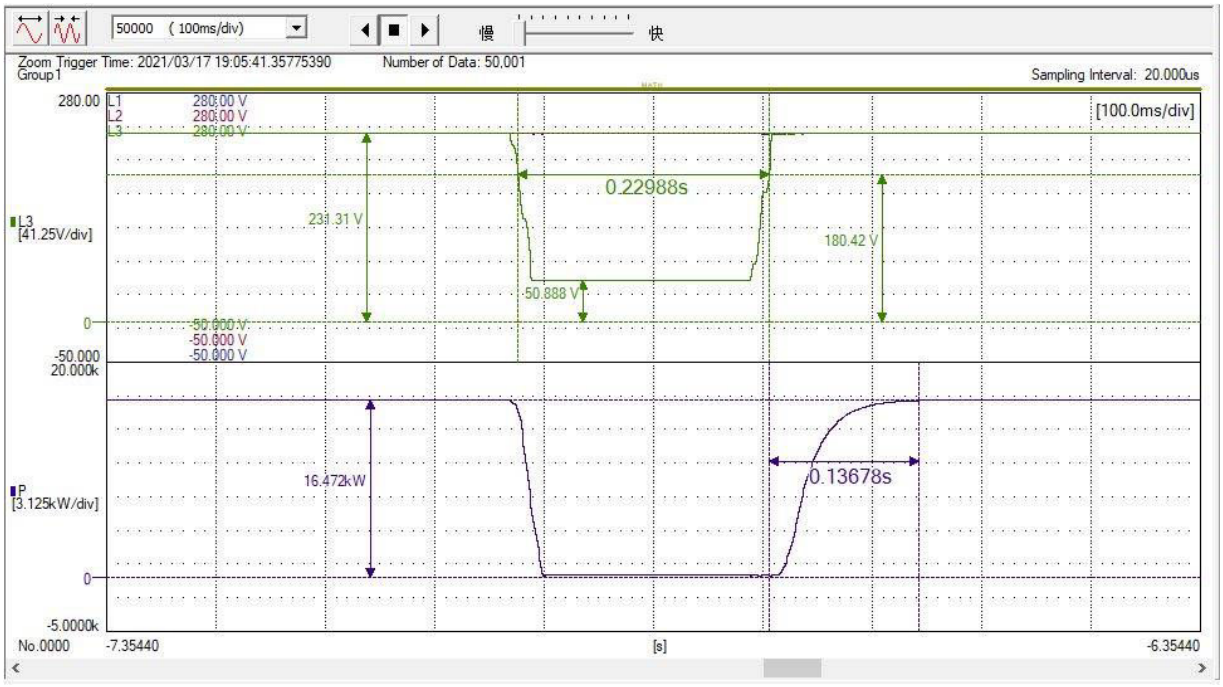
Test 2 c) – one-phase symmetrical fault



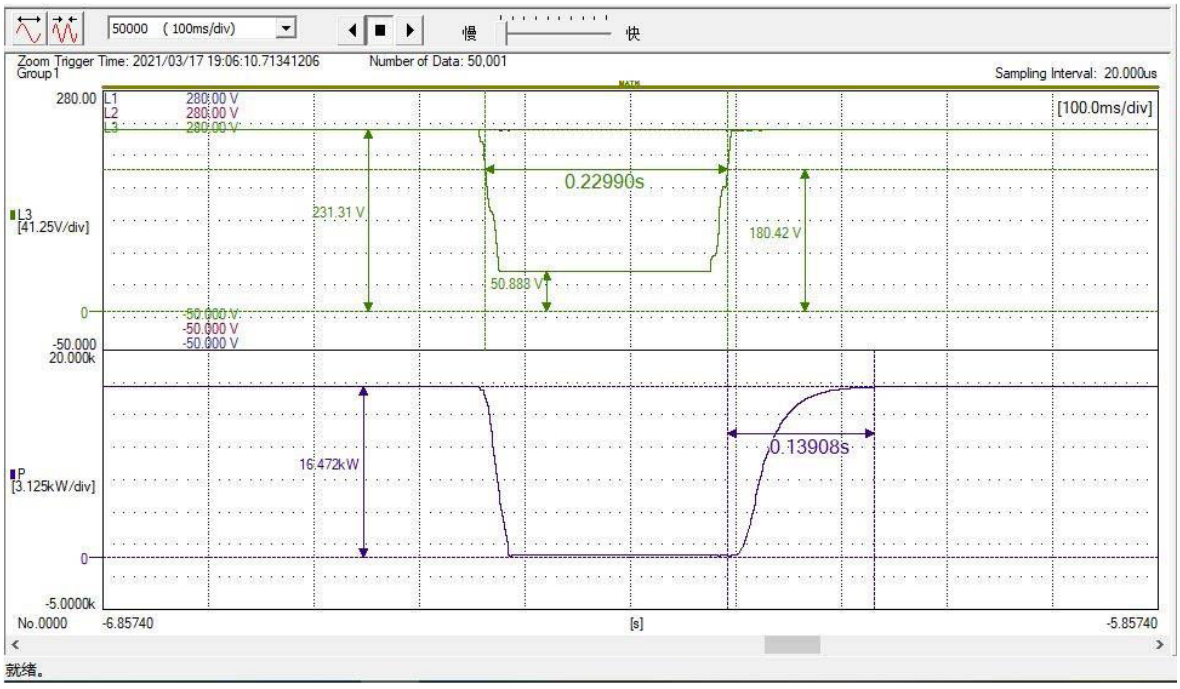
Test 2 c) – one-phase symmetrical fault



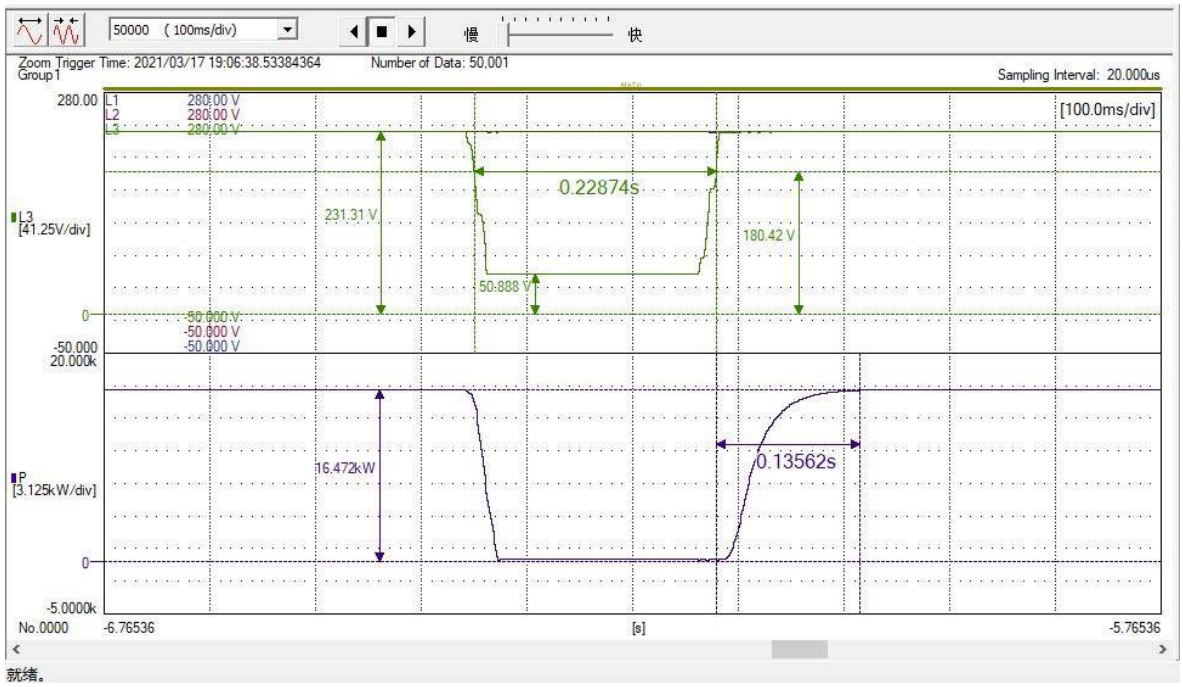
Test 3 a) – one-phase symmetrical fault



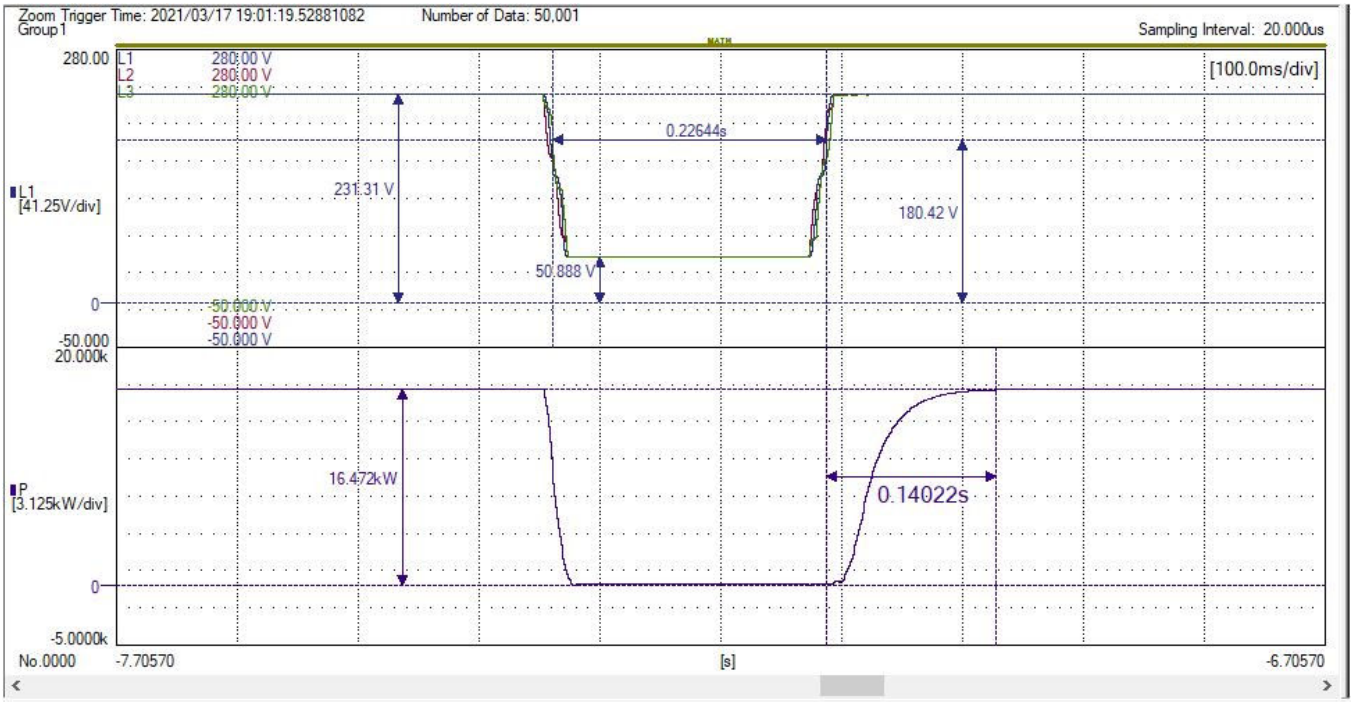
Test 3 b) – one-phase symmetrical fault



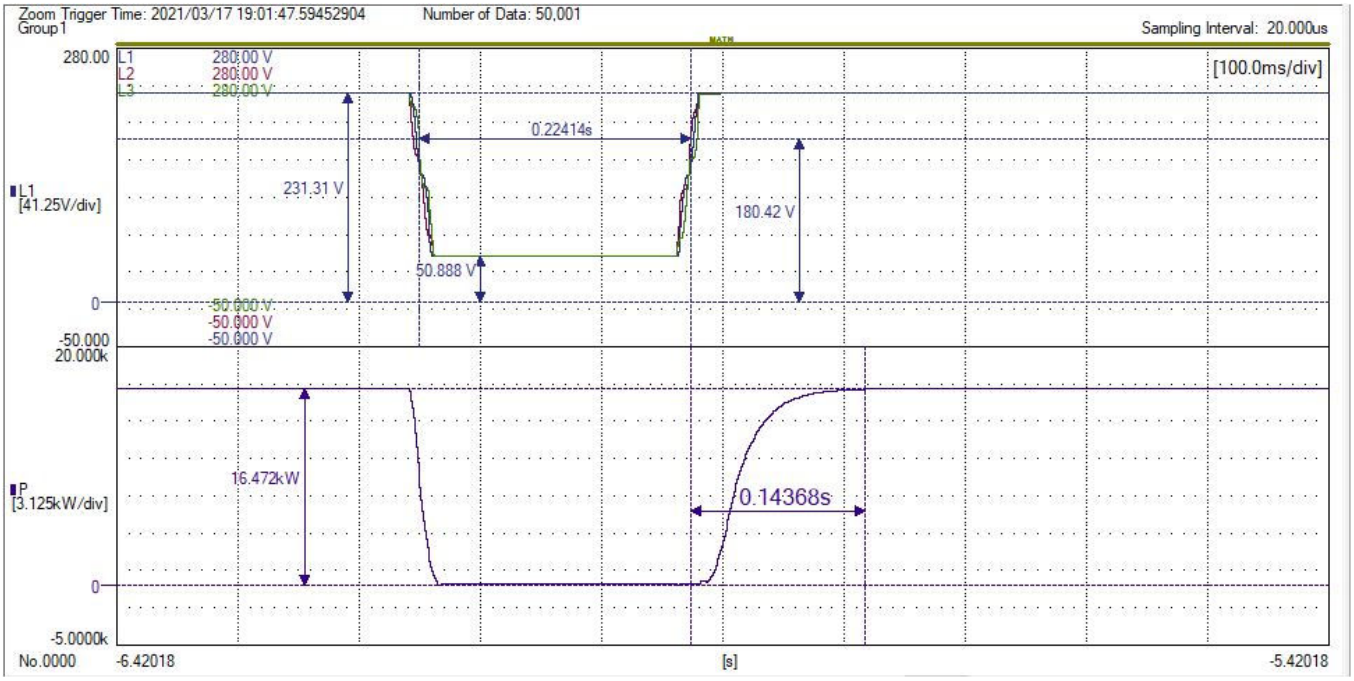
Test 3 c) – one-phase symmetrical fault



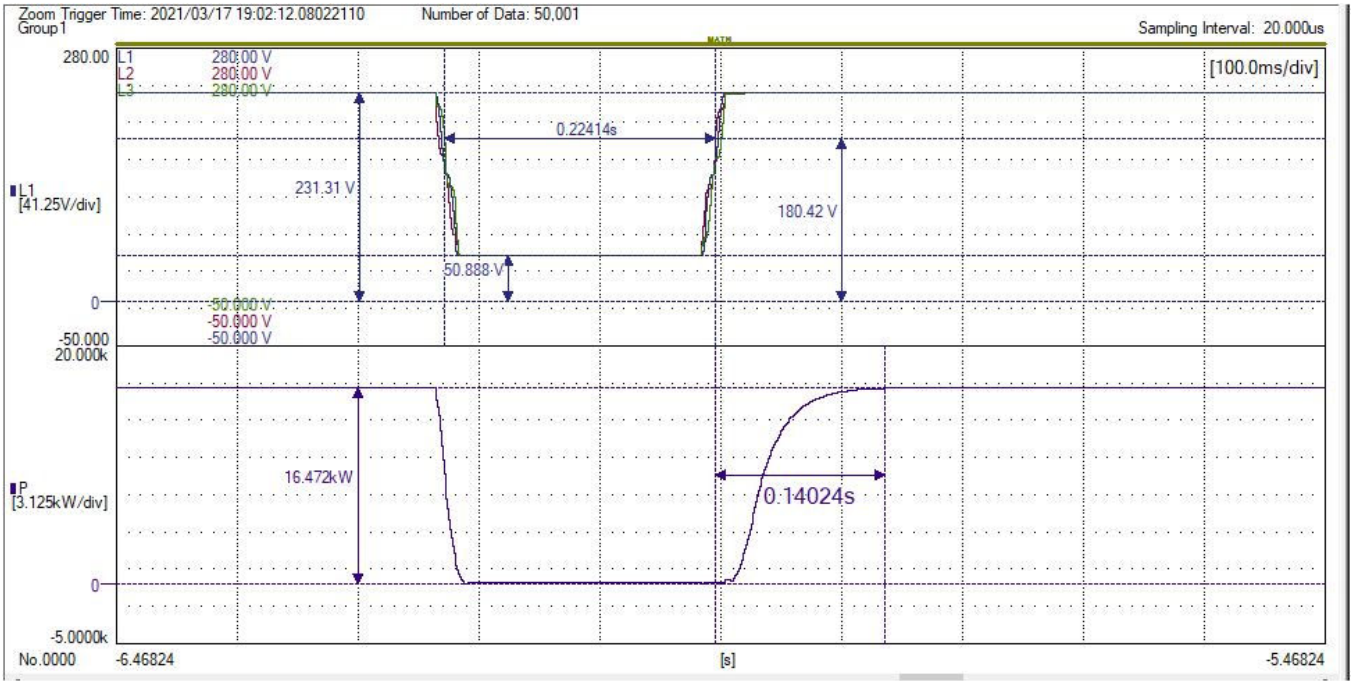
Test 4 a) – all-phase symmetrical fault



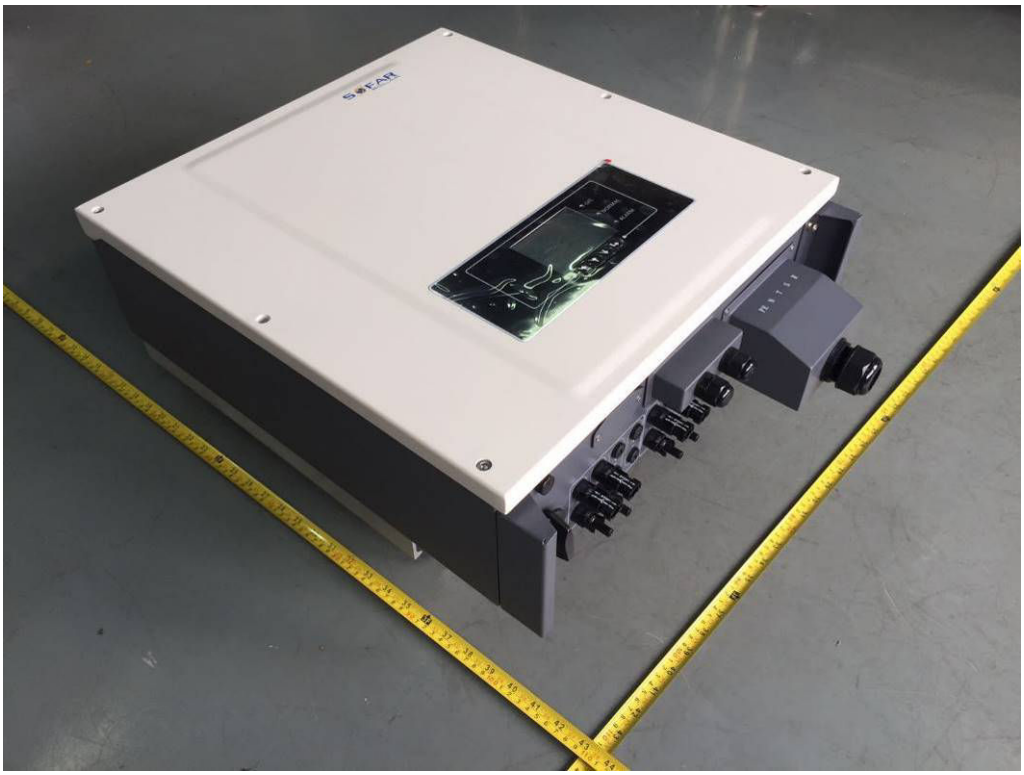
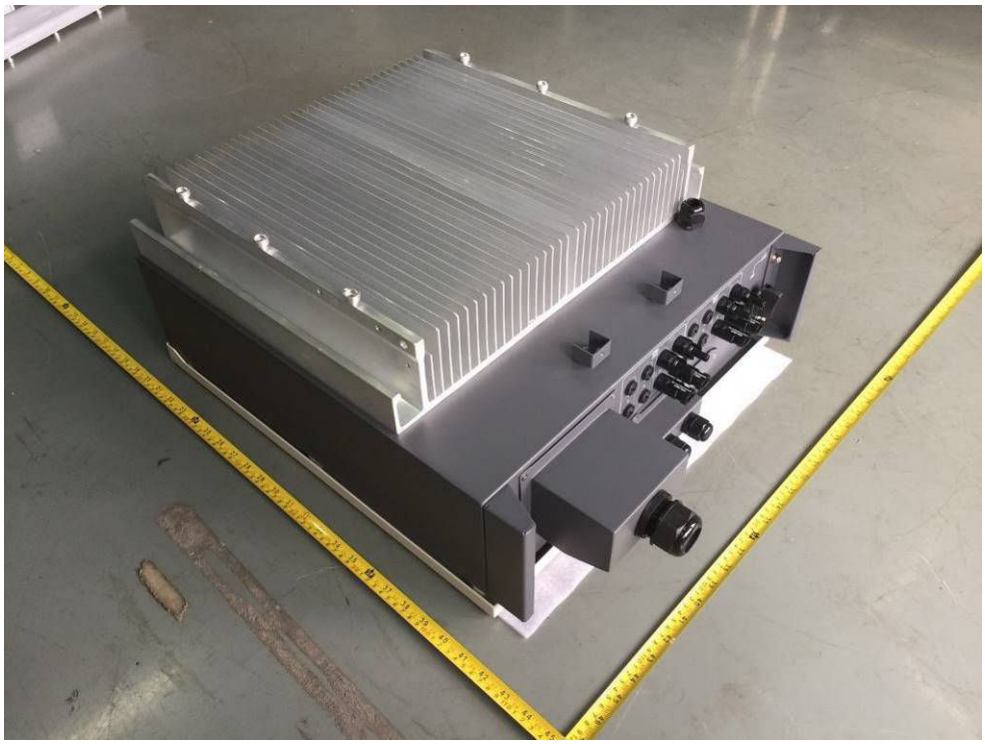
Test 4 b) –all-phase symmetrical fault

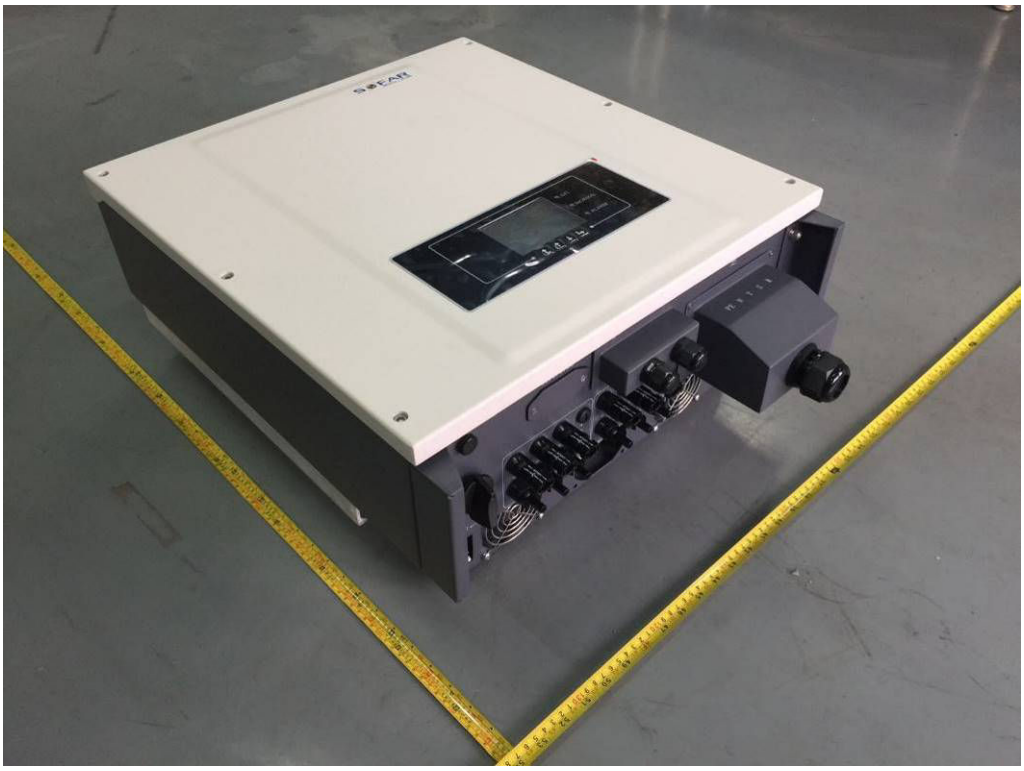
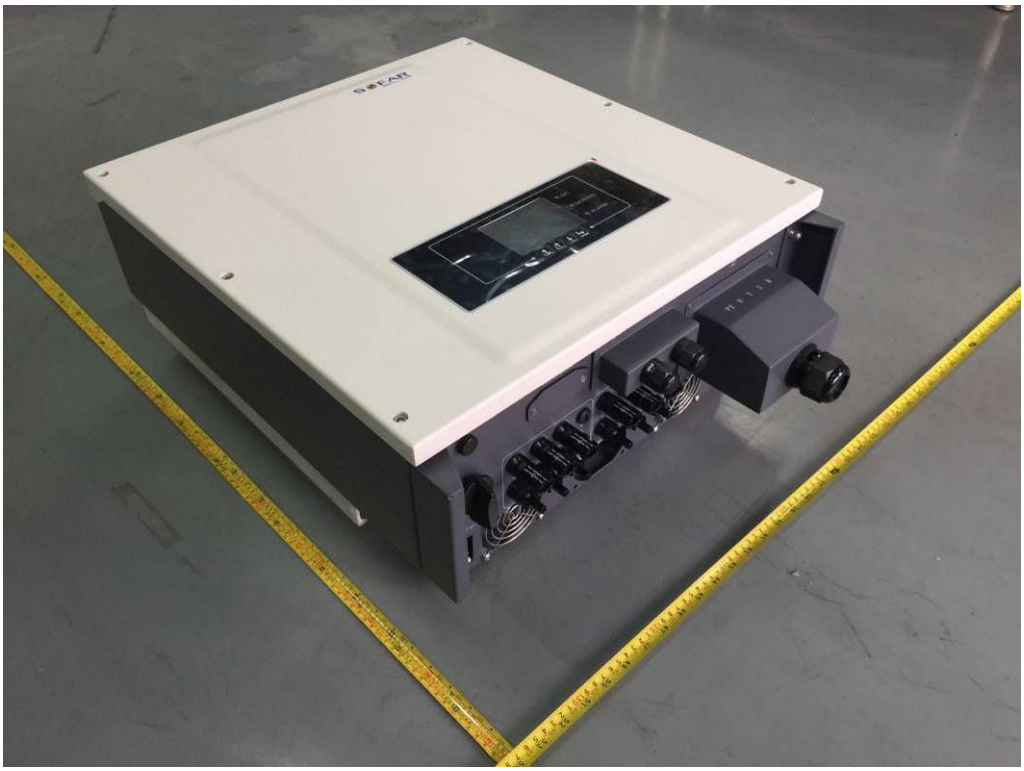


Test 4 c) –all-phase symmetrical fault

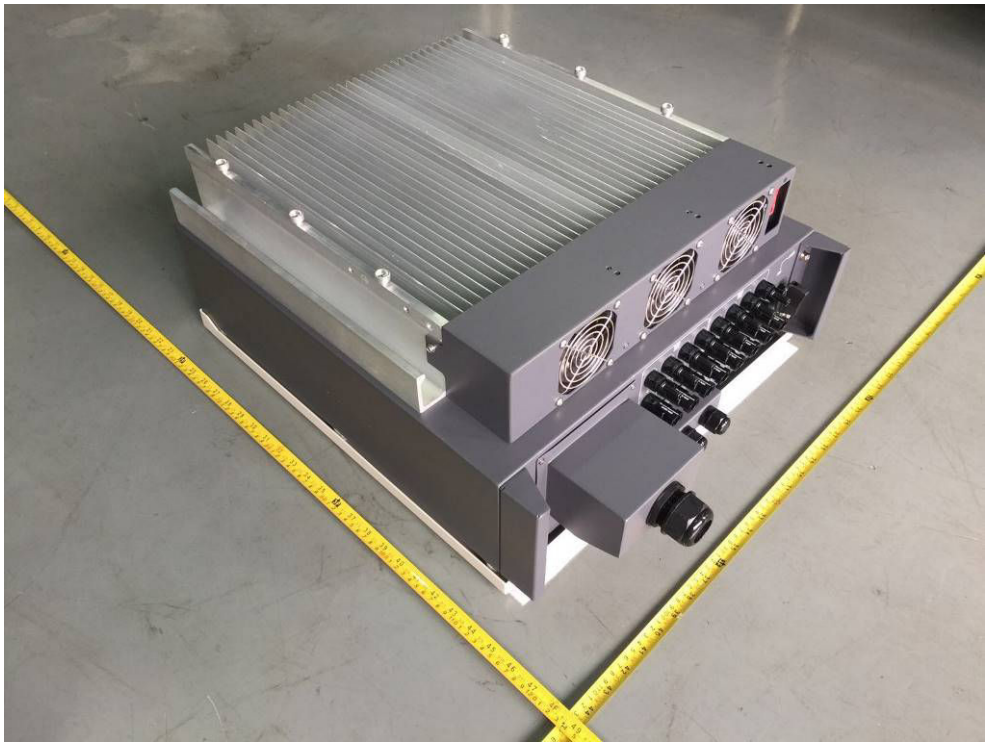


Annex No. 1
Pictures of the unit

Enclosure front view: SOFAR 20000TL-G2

Enclosure rear view: SOFAR 20000TL-G2


Enclosure front view: SOFAR 25000TL-G2

Enclosure rear view: SOFAR 25000TL-G2


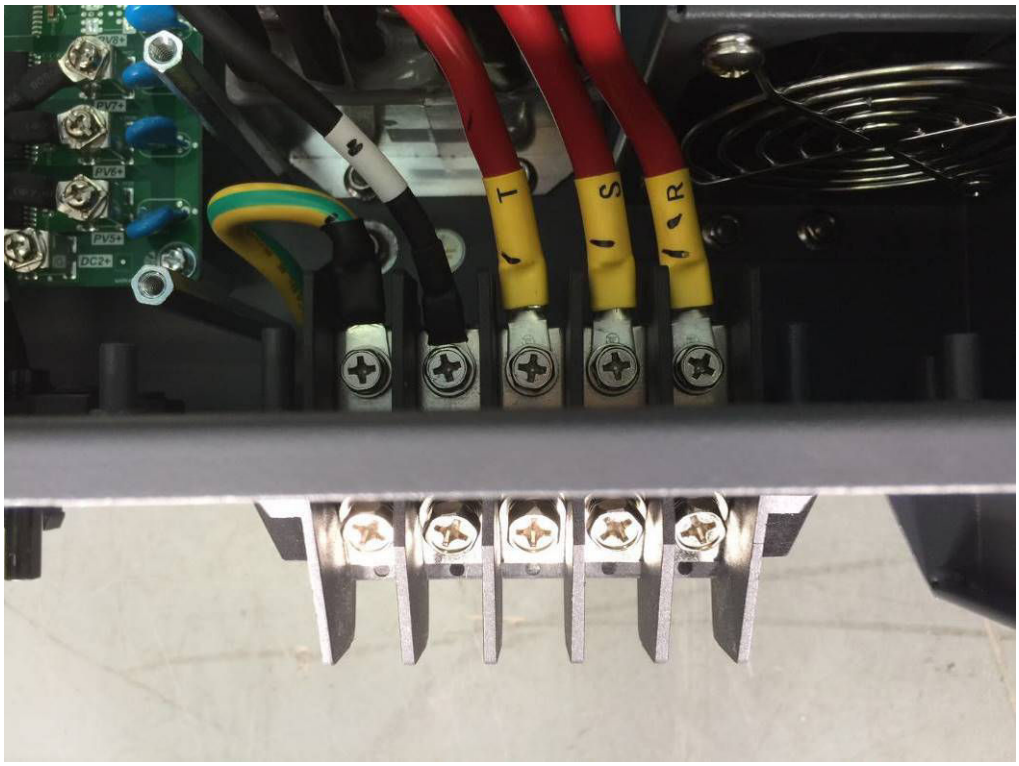
Enclosure front view: SOFAR 30000TL-G2, SOFAR 33000TL-G2

Enclosure rear view: SOFAR 30000TL-G2, SOFAR 33000TL-G2


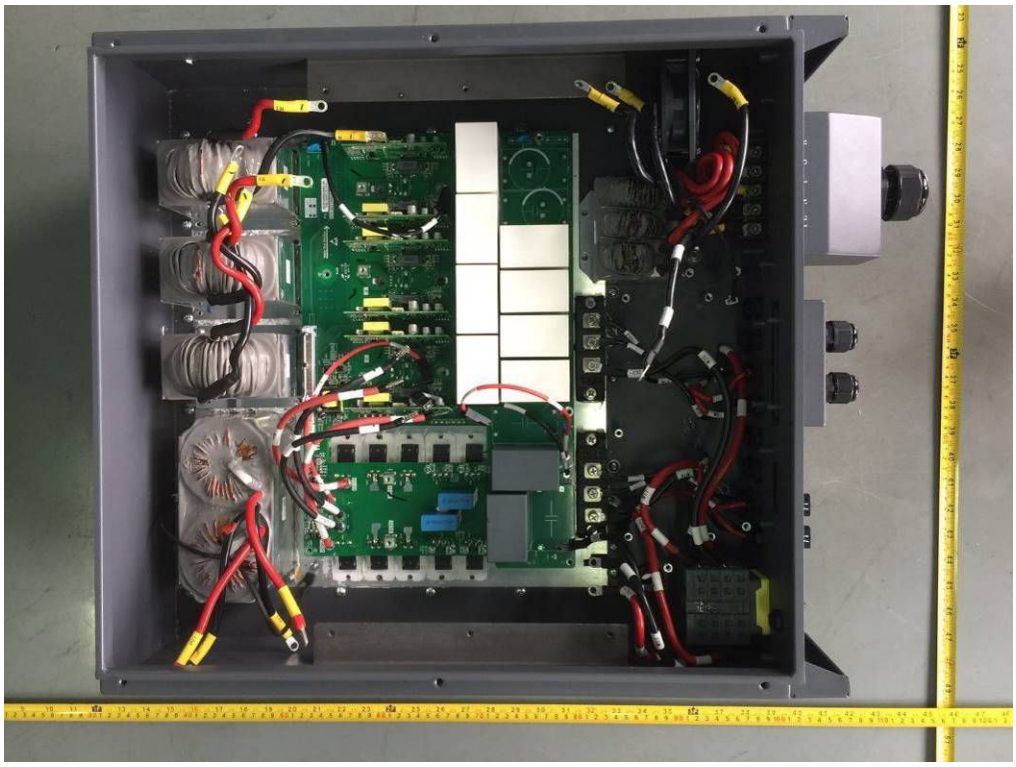
Enclosure terminal view: SOFAR 25000TL-G2

Enclosure terminal view: SOFAR 25000TL-G2

Enclosure terminal view: SOFAR 30000TL-G2, SOFAR 33000TL-G2

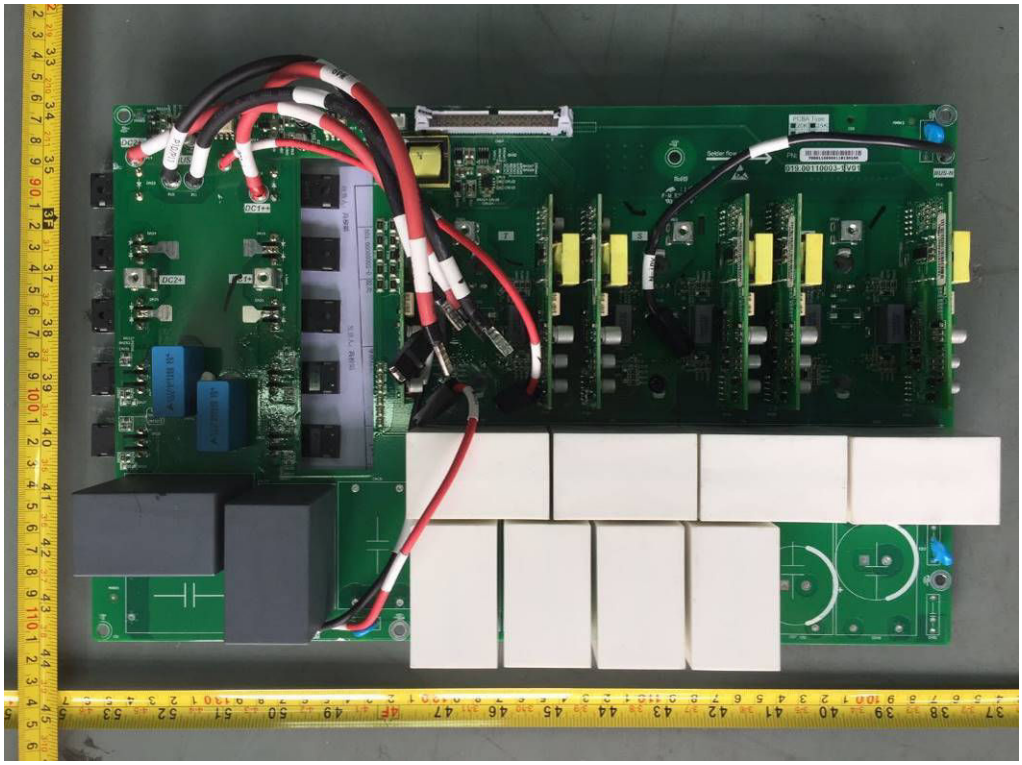
AC output terminal


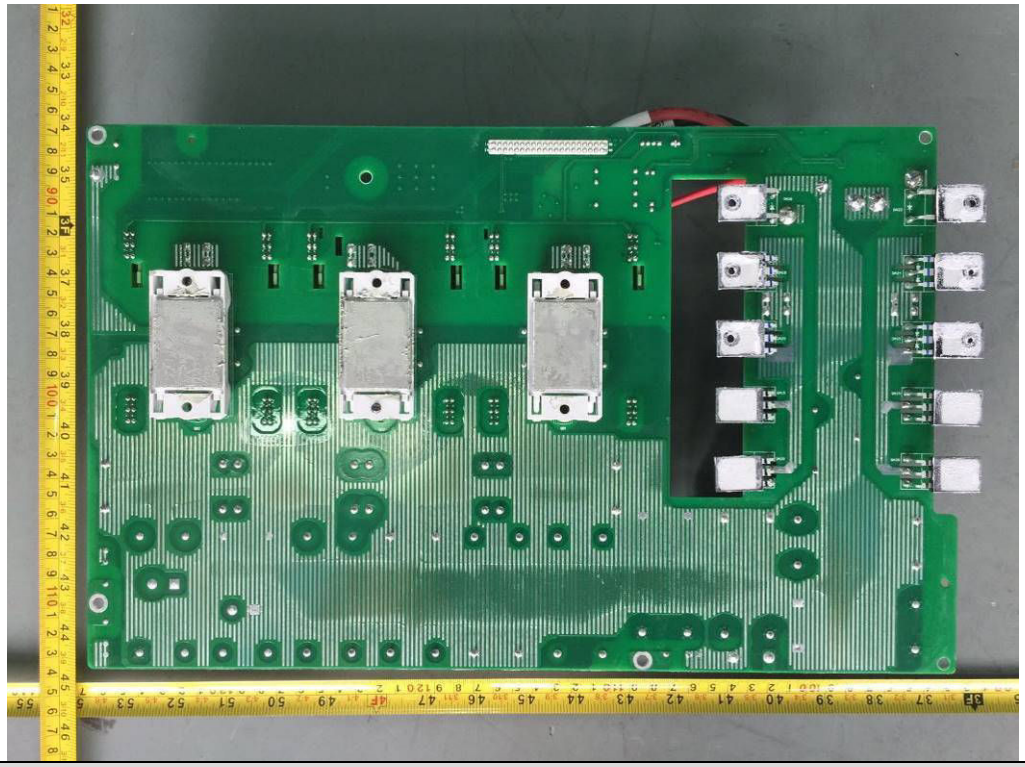
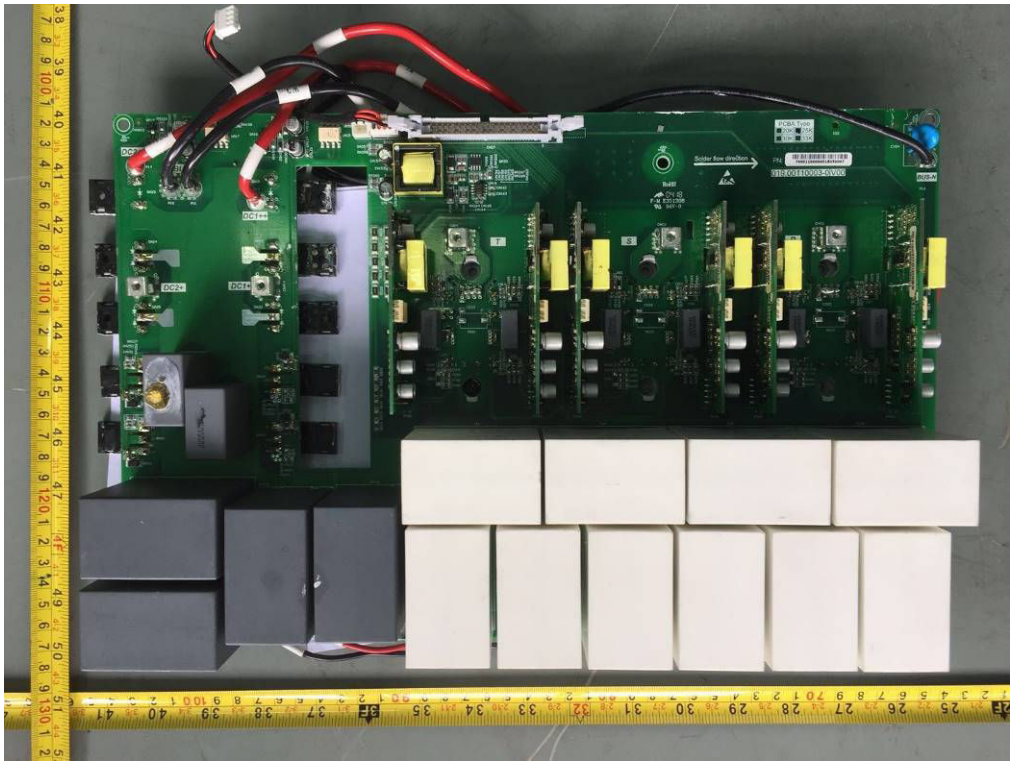
Internal view: SOFAR 20000TL-G2

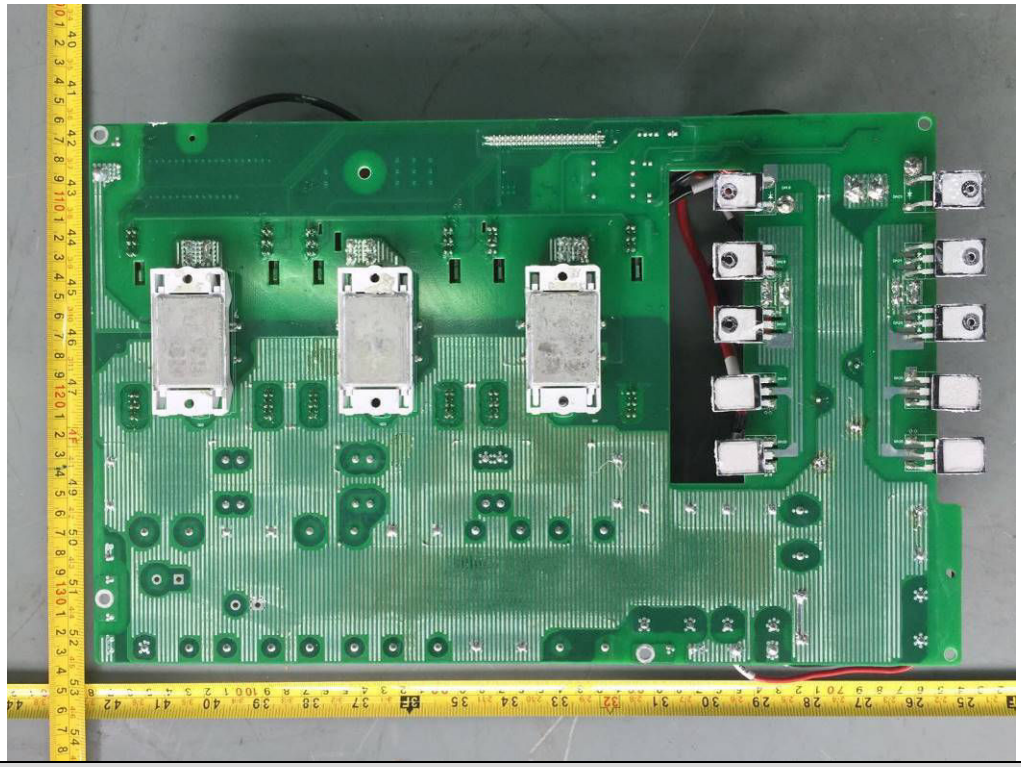
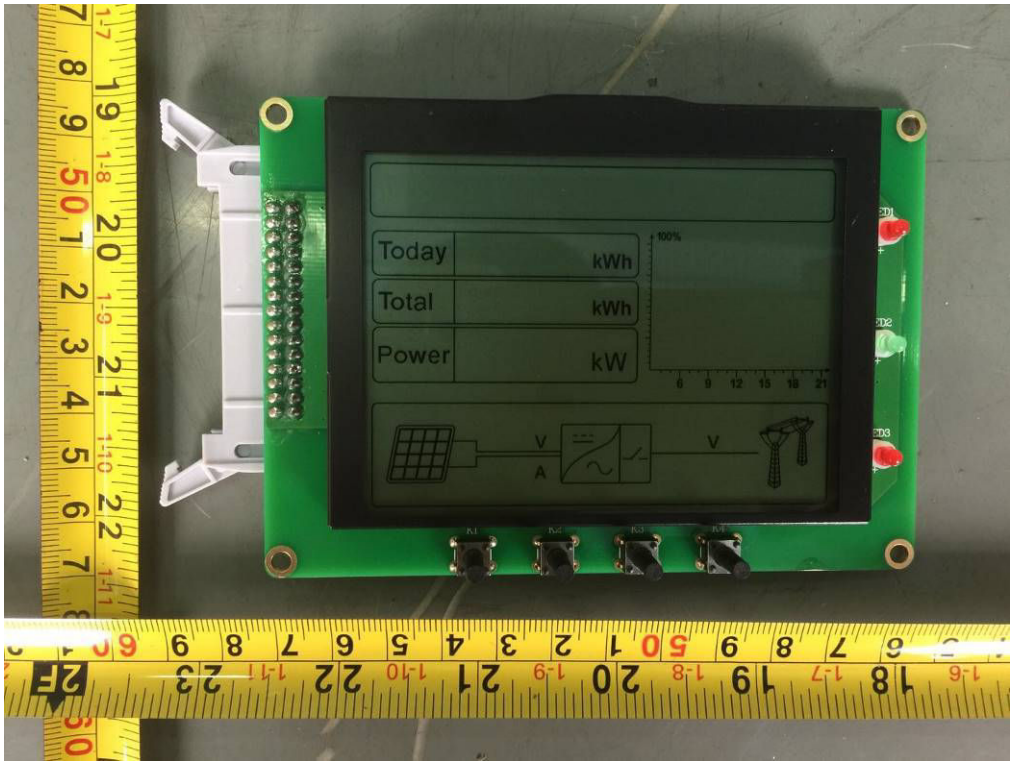


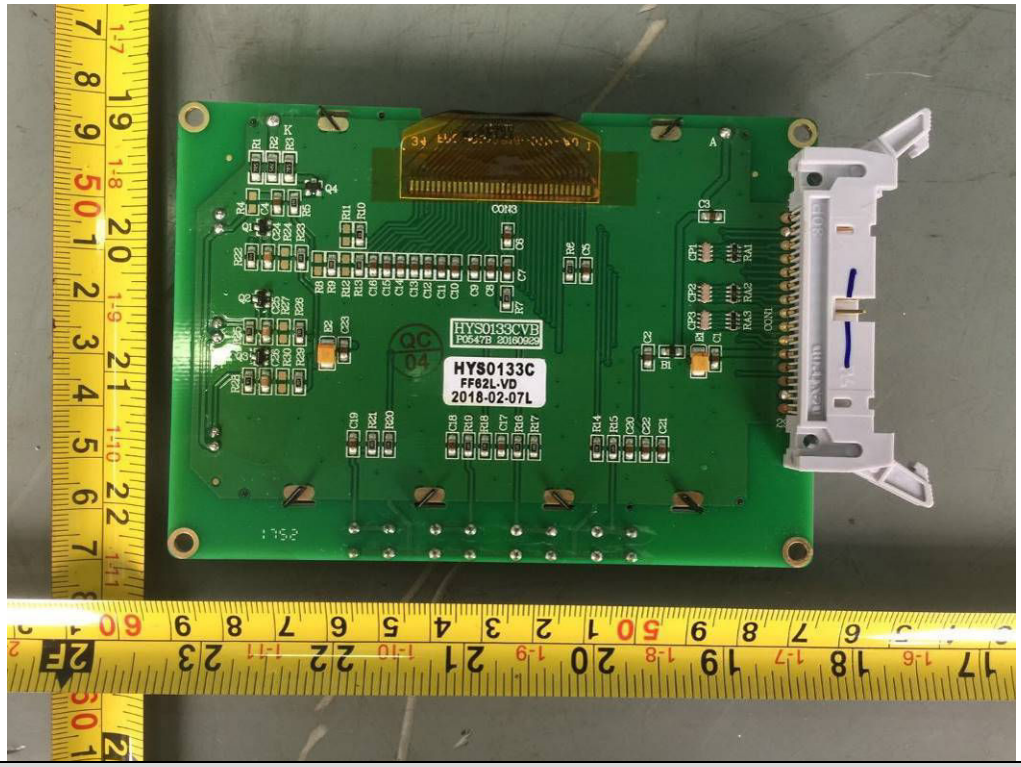
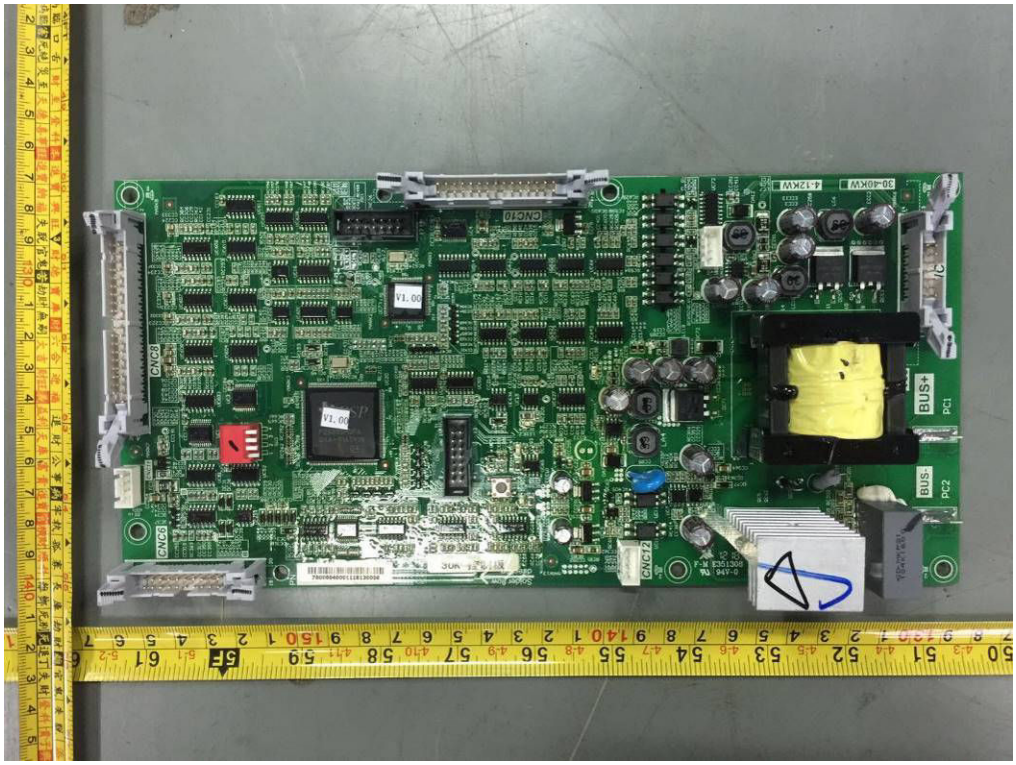
Internal view: SOFAR 25000TL-G2

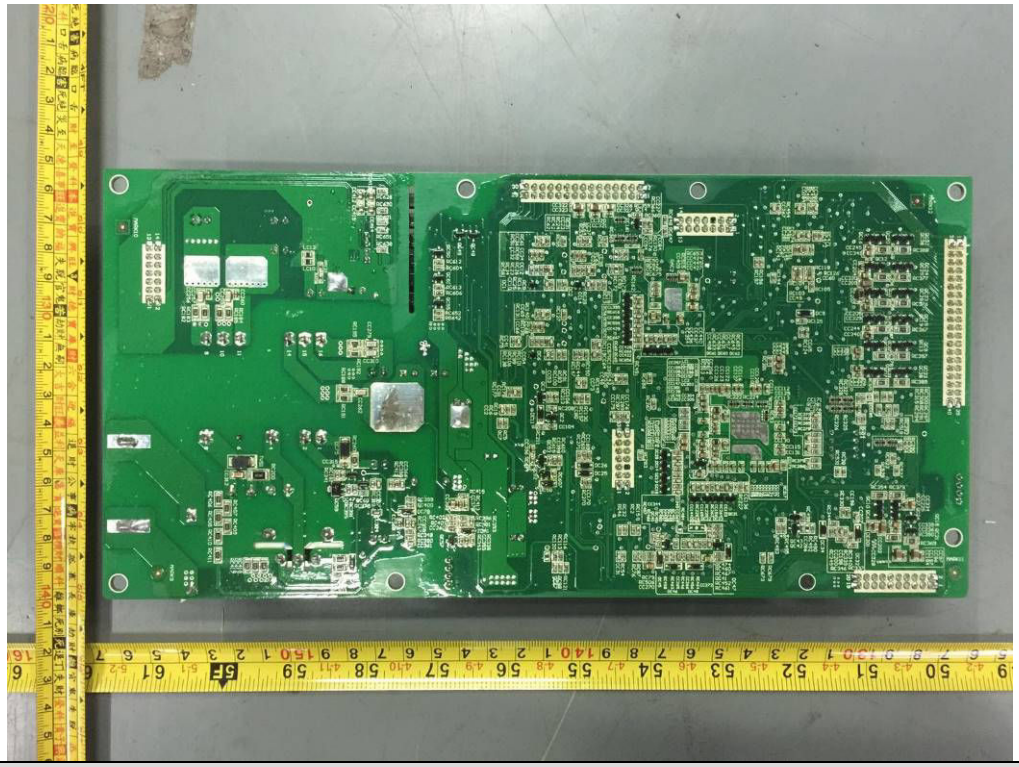
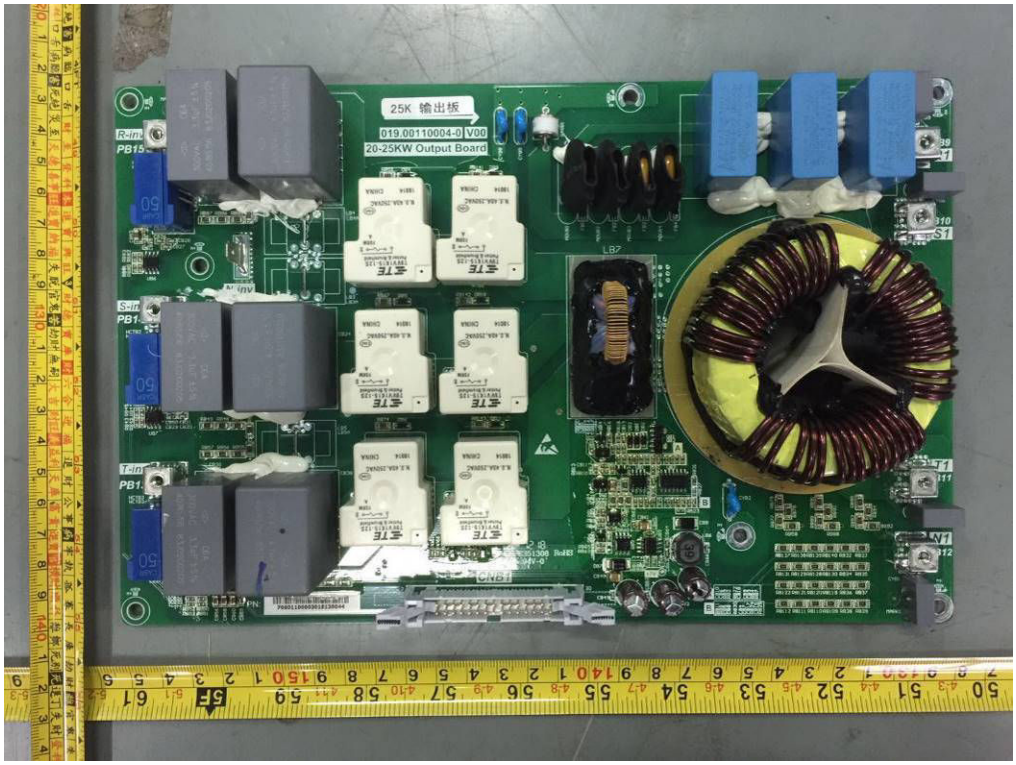
Internal view: SOFAR 30000TL-G2, SOFAR 33000TL-G2

Main board-component side view: SOFAR 20000TL-G2, SOFAR 25000TL-G2


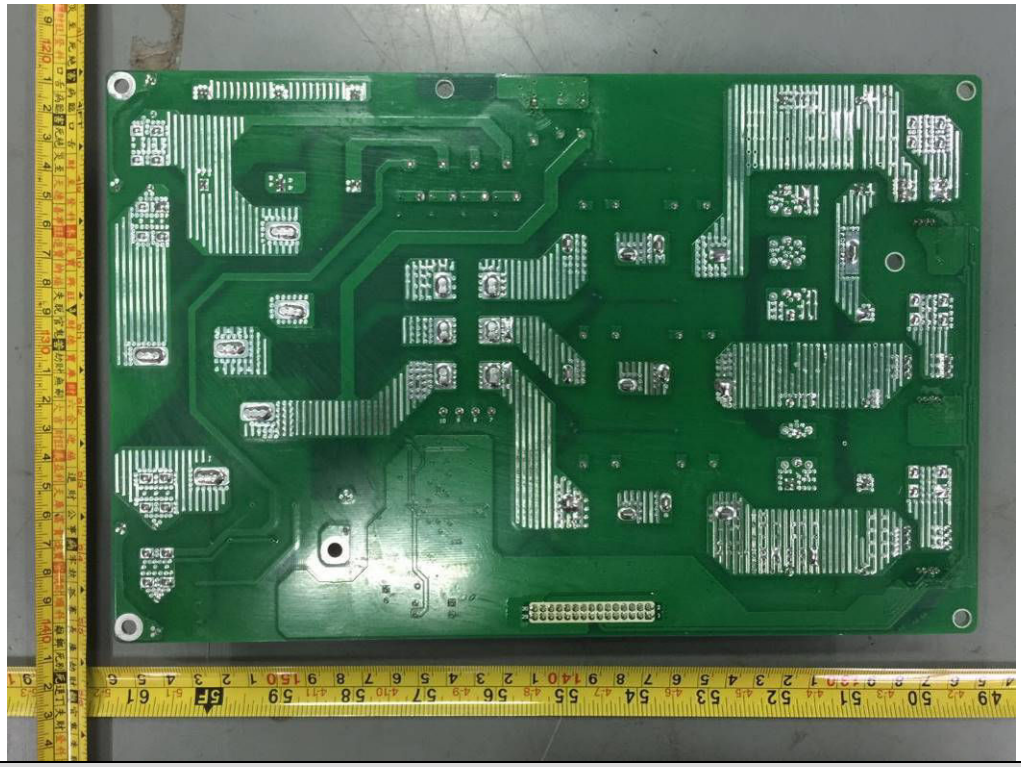
Main board- solder side view: SOFAR 20000TL-G2, SOFAR 25000TL-G2

Main board-component side view: SOFAR 30000TL-G2, SOFAR 33000TL-G2


Main board- solder side view: SOFAR 30000TL-G2, SOFAR 33000TL-G2

Display board-component side view


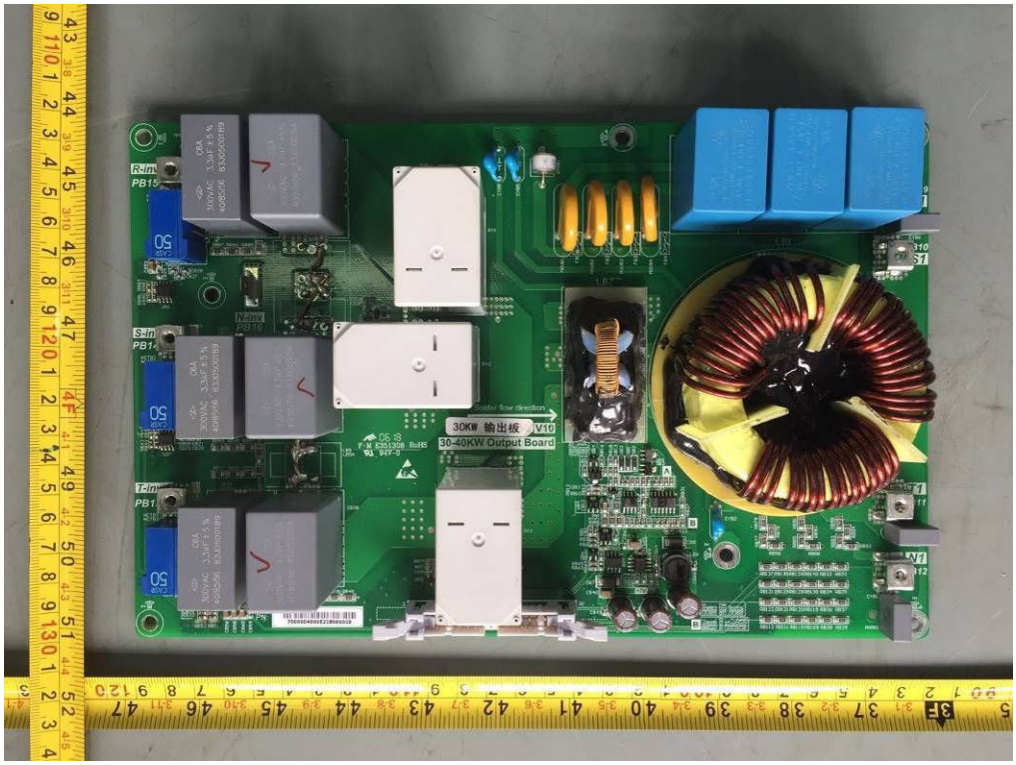
Display board-solder side view

Control board- component side view


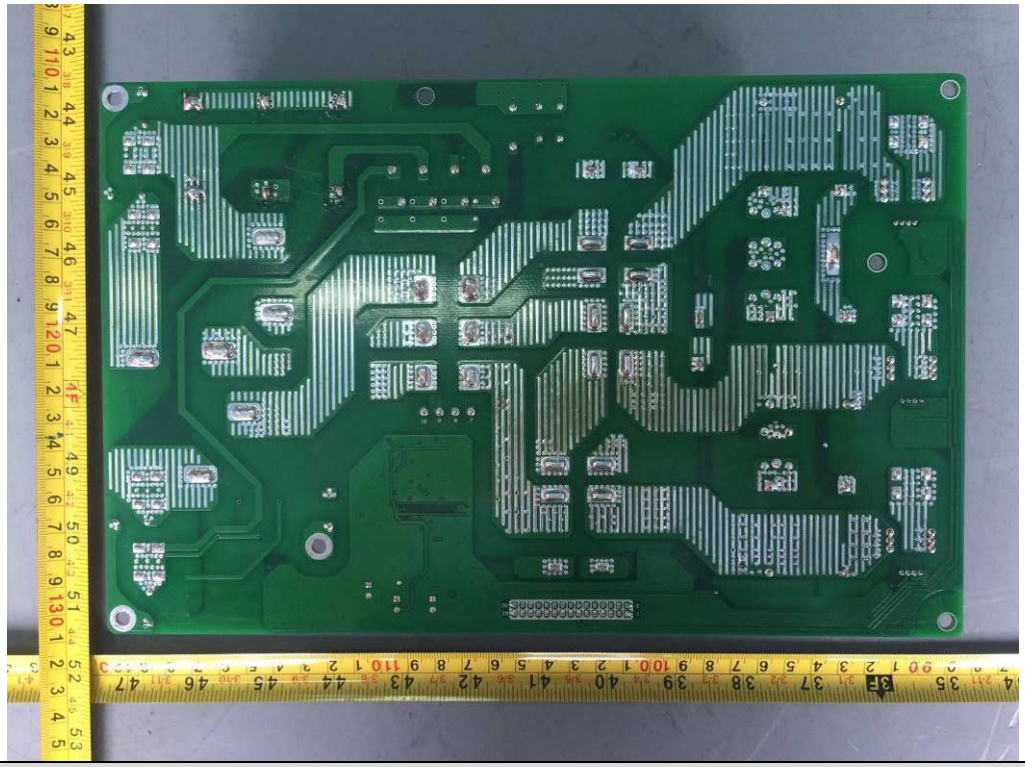
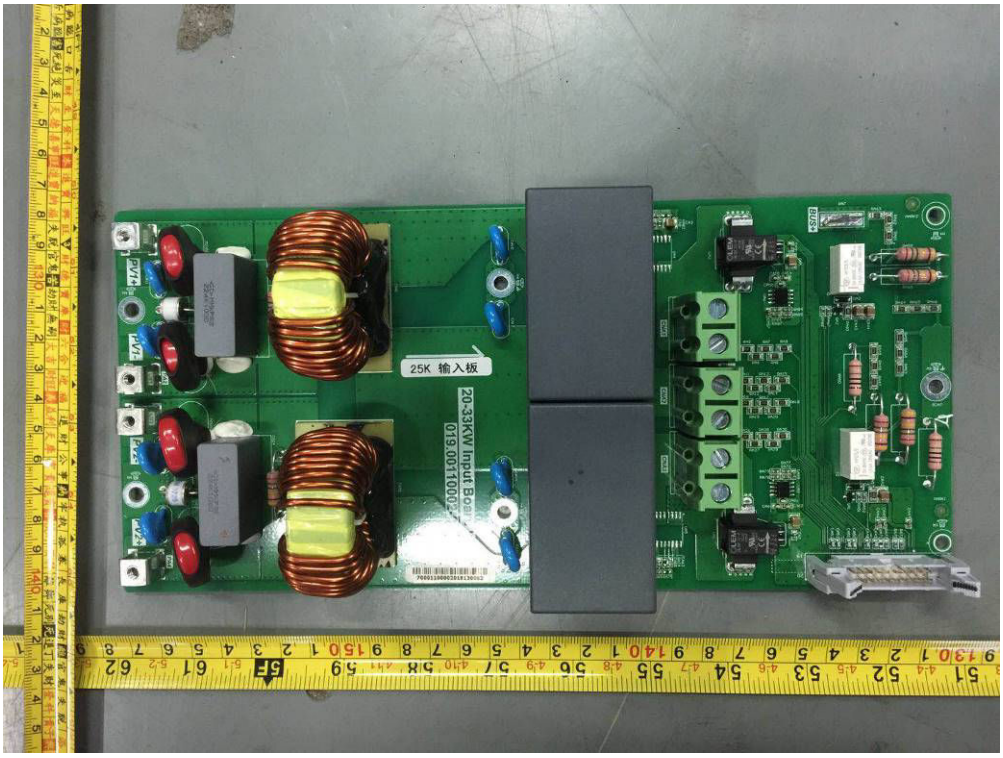
Control board- solder side view

AC output board- component side view: SOFAR 20000TL-G2, SOFAR 25000TL-G2


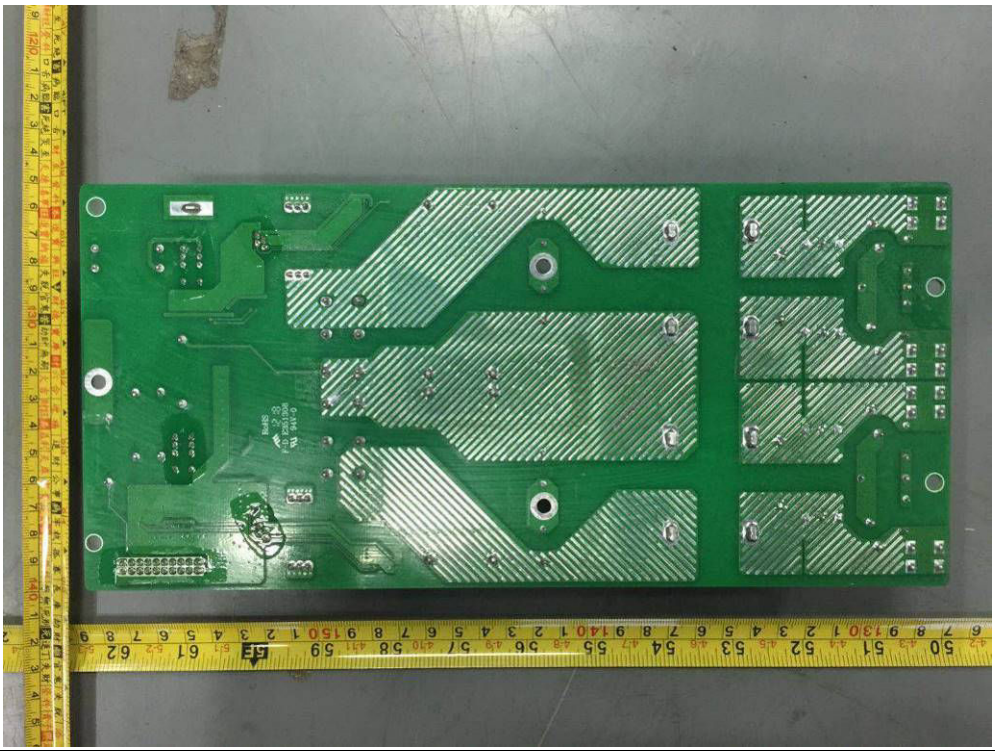
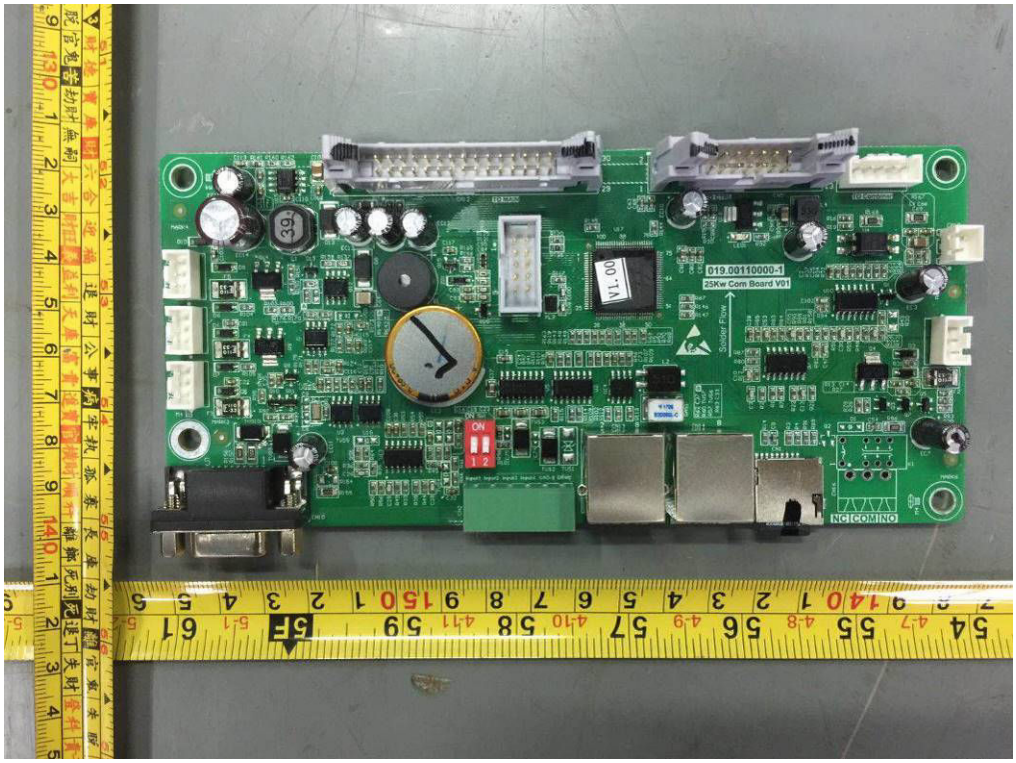
AC output board-solder side view: SOFAR 20000TL-G2, SOFAR 25000TL-G2

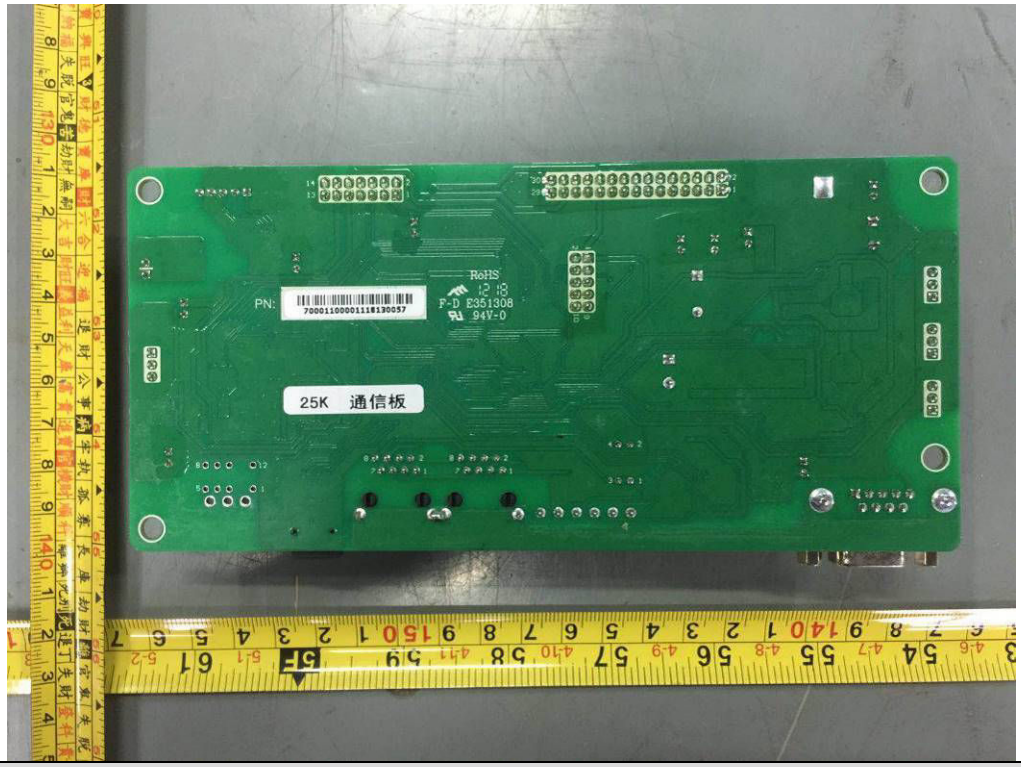
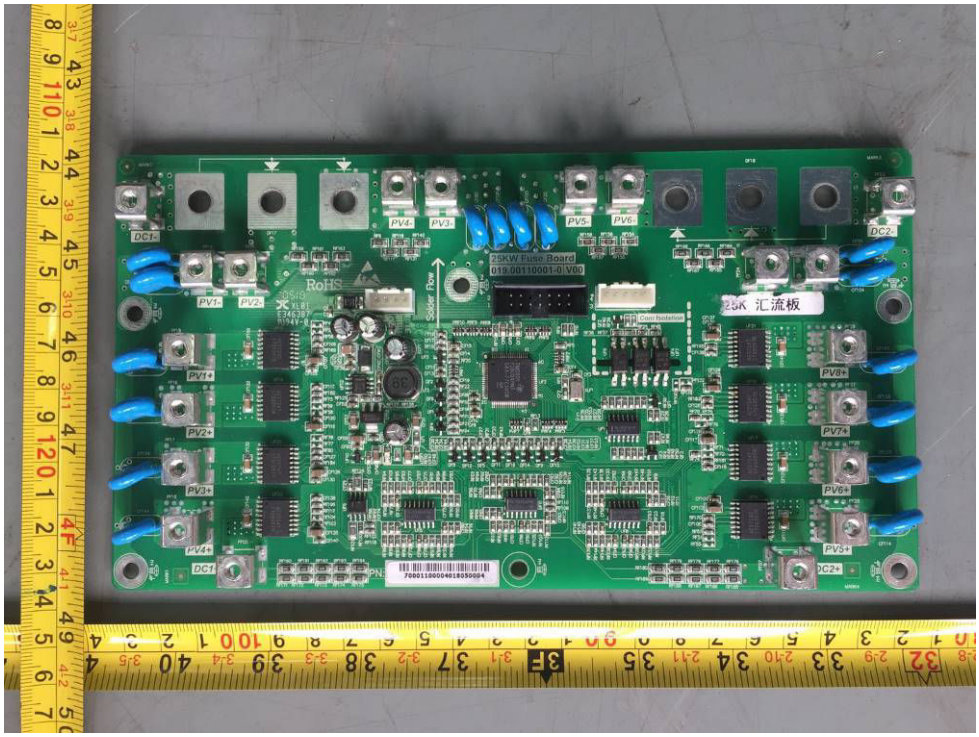


AC output board- component side view: SOFAR 30000TL-G2, SOFAR 33000TL-G2

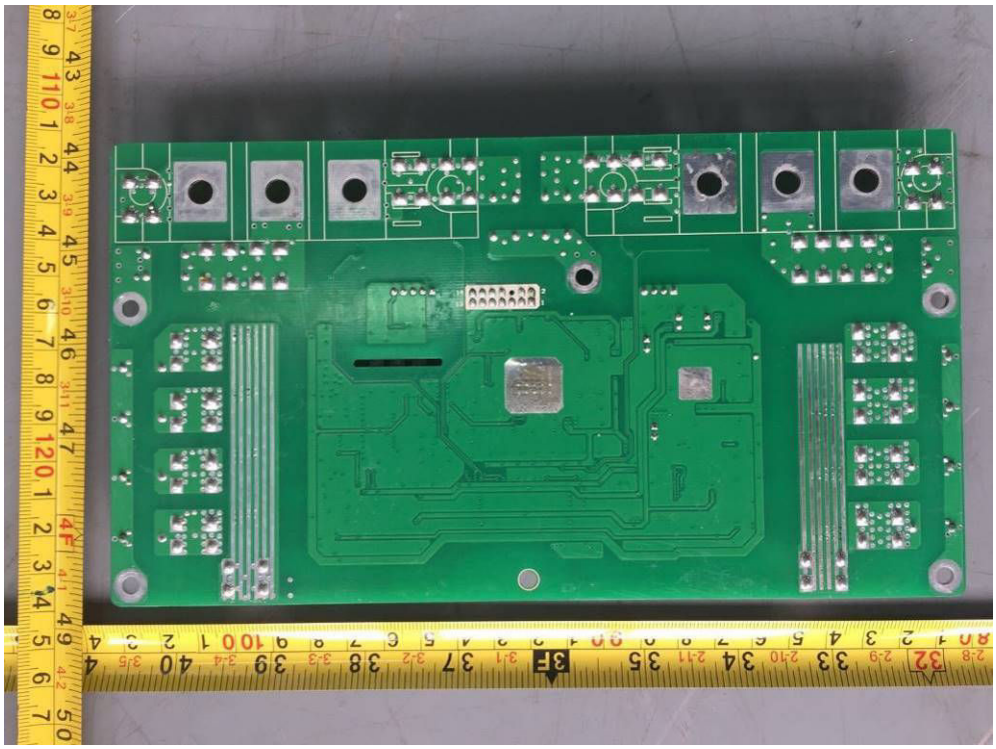


AC output board-solder side view: SOFAR 30000TL-G2, SOFAR 33000TL-G2

Output board-component side view


Output board-solder side view

Communication board-component side view


Communication board-solder side view

BUS board-component side view: **SOFAR 25000TL-G2, SOFAR 30000TL-G2, SOFAR 33000TL-G2, SOFAR 30000TL-G2**


BUS board-solder side view:
SOFAR 25000TL-G2, SOFAR 30000TL-G2, SOFAR 33000TL-G2, SOFAR 30000TL-G2



Annex No. 2
Test Equipment list

Date(s) of performance test: 2020-09-11 to 2020-09-14

Equipment	Internal No.	Manufacturer	Type	Serial No.	Next Calibration date
Power Analyser	A4080002DG	YOKOGAWA	WT3000	91M210852	Jun. 16, 2021
AC Source	A7040019DG	Chroma	61512	61512000439	Monitored by Power Analyser
	A7040020DG	Chroma	61512	61512000438	
DC Simulation Power Supply	A7040015DG	Chroma	62150H-1000S	62150EF00488	
	A7040016DG	Chroma	62150H-1000S	62150EF00490	
	A7040017DG	Chroma	620028	620028EF00120	
RLC Load	A7150027DG	Qunling	ACLT-3803H	93VOO2869	
Eight Channel Digital Phosphor Oscilloscope	A4089017DG	YOKOGAWA	DL850	91N726247	Sep. 23, 2021
Oscilloscope probe	A4089008DG	Tektronix	TPP1000	C008230	Aug. 10, 2021
	A4089010DG	Tektronix	TPP1000	C008228	Aug. 10, 2021
	A4089011DG	Tektronix	TPP1000	C008229	Aug. 10, 2021
Current transducer	A1060007DG	YOKOGAWA	CT200	1130700012	Sep. 02, 2021
	A1060008DG	YOKOGAWA	CT200	1130700017	Sep. 02, 2021
	A1060012DG	YOKOGAWA	CT200	1130700018	Sep. 02, 2021
Oscilloscope	//	KEYSIGHT	DSX3014T	MY57231269	Jan. 13, 2021
Oscilloscope current probe	//	CYBERTEK	CP1000A	C181000922	Jan. 13, 2021
	//	CYBERTEK	CP1000A	C181000925	Jan. 13, 2021
	//	CYBERTEK	CP1000A	C181000929	Jan. 13, 2021
	//	CYBERTEK	CP1000A	C181000931	Jan. 13, 2021
Oscilloscope probe	//	SANHUA	SI-9110	152627	Jan. 13, 2021
	//	SIALENT	DS5034X	SDS5XEAC3R0011	Jan. 13, 2021
	//	AGILENT	N2863B	YF0139	Jan. 13, 2021

End of Test Report