TEST REPORT

IEC 62116

Test procedure of islanding prevention measures for utility-interconnected photovoltaic inverters

Report reference number ..... :	**PV200511N080-1-R1**
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Total number of pages ..... ::	36
Testing laboratory name ..... :	**Bureau Veritas Shenzhen Co., Ltd. Dongguan Branch**
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Accrediation ..... :	ilac-MRA A2LA ACCREDITED Certificate # 2951.01
Applicant's name..... :	**Shenzhen SOFARSOLAR Co., Ltd.**
Address ..... :	401, Building 4, AnTongDa Industrial Park, District 68, XingDong Community, XinAn Street, BaoAn District, Shenzhen, China
Test specification	
Standard..... :	IEC 62116:2014
Test Report Form No. ..... :	IEC/EN 62116 VER.2
TRF Originator ..... :	Bureau Veritas Shenzhen Co., Ltd. Dongguan Branch
Master TRF ..... :	Dated 2020-03-11
Test item description ..... :	**Solar Grid-tied Inverter**
Trademark..... :	SOFAR SOLAR
Model / Type ..... :	SOFAR 15KTLX-G3, SOFAR 17KTLX-G3, SOFAR 20KTLX-G3, SOFAR 22KTLX-G3, SOFAR 24KTLX-G3,

Ratings .....:	SOFAR 15KTLX-G3	SOFAR 17KTLX-G3	SOFAR 20KTLX-G3	SOFAR 22KTLX-G3	SOFAR 24KTLX-G3
Input DC voltage [V] .....:	Max. 1100Vd.c.				
MPP DC voltage range [V] .....:	140-1000Vd.c.				
Input DC current [A] .....:	26,0A / 26,0A				
Output AC voltage [V] .....:	380/400Va.c., 3W+N+PE; 50/60Hz				
Max. Output AC current [A] .....:	23,9	27,1	31,9	35,1	38,3
Nominal Output power [kW] .....:	15,0	17,0	20,0	22,0	24,0
Maximum Output power [kVA] .....:	16,5	18,7	22,0	24,2	26,4

Testing Location ..... :	**Bureau Veritas Shenzhen Co., Ltd. Dongguan Branch**
Address ..... :	No. 96, Guantai Road (Houjie Section), Houjie Town, Dongguan City, Guangdong Province, 523942, People's Republic of China
Tested by (name and signature) ..... :	Lukes Lin 
Approved by (name and signature) ..... :	James Huang 
Manufacturer's name ..... :	**Shenzhen SOFARSOLAR Co., Ltd.**
Manufacturer address ..... :	401, Building 4, AnTongDa Industrial Park, District 68, XingDong Community, XinAn Street, BaoAn District, Shenzhen, China
Factory's name 1 ..... :	**Dongguan SOFAR SOLAR Co.,Ltd**
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Document History			
Date	**Internal reference**	**Modification / Change / Status**	**Revision**
2020-10-21	Lukes Lin	Initial report was written	0
2020-11-20	Lukes Lin	Correct the report number.	R1
Supplementary information:			

Test items particulars

Equipment mobility ..... :	Permanent connection
Operating condition ..... :	Continuous
Class of equipment ..... :	Class I
Protection against ingress of water .. :	IP65 according to EN 60529
Mass of equipment [kg] ..... :	Approx. 20,0 kg for SOFAR 15KTLX-G3; Approx. 22,0 kg for SOFAR 17KTLX-G3, SOFAR 20KTLX-G3; Approx. 23,0 kg for SOFAR 22KTLX-G3, SOFAR 24KTLX-G3;

Test case verdicts

Test case does not apply to the test object ..... :	N/A
Test item does meet the requirement ..... :	P(ass)
Test item does not meet the requirement ..... :	F(ail)

Testing

Date of receipt of test item ..... :	2020-05-11
Date(s) of performance of test ..... :	2020-05-11 to 2020-09-28

General remarks:

The test result presented in this report relate only to the object(s) tested.
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”(see Annex #)" refers to additional information appended to the report.
"(see appended table)" refers to a table appended to the report.

Throughout this report a comma is used as the decimal separator.

This report is to replace the earlier Test Report Ref. No. **PV200111N080-1** issued by Bureau Veritas Shenzhen Co., Ltd. Dongguan Branch, dated on 2020-10-21.

This Test Report consists of the following documents:

1. Test Results
2. Annex No. 1 – Pictures of the unit
3. Annex No. 2 – Test equipment list

Copy of marking plates:

SOFAR SOLAR Solar Grid-tied Inverter

Model No:	SOFAR 15KTLX-G3
Max.DC Input Voltage	1100V
Operating MPPT Voltage Range	140~1000V
Max. Input Current	26A/26A
Max. PV Isc	36A/36A
Nominal Grid Voltage	3/N/PE,380/400V
Max.Output Current	3x23.9A
Nominal Grid Frequency	50/60Hz
Nominal Output Power	15000W
Max.Output Power	16500VA
Power Factor	1(adjustable+/-0.8)
Ingress Protection	IP65
Operating Temperature Range	-30℃~+60℃
Protective Class	Class I

Made in China

Manufacturer : Shenzhen SOFAR SOLAR Co.,Ltd.
Address : 401, Building 4, AnTongDa Industrial Park, District 68, XingDong Community,XinAn Street, BaoAn District, Shenzhen, China
VDE0126-1-1,VDE-AR-N4105,G99,IEC61727
IEC62116,UTE C15-712-1,AS4777

SOFAR SOLAR Solar Grid-tied Inverter

Model No:	SOFAR 17KTLX-G3
Max.DC Input Voltage	1100V
Operating MPPT Voltage Range	140~1000V
Max. Input Current	26A/26A
Max. PV Isc	36A/36A
Nominal Grid Voltage	3/N/PE,380/400V
Max.Output Current	3x27.1A
Nominal Grid Frequency	50/60Hz
Nominal Output Power	17000W
Max.Output Power	18700VA
Power Factor	1(adjustable+/-0.8)
Ingress Protection	IP65
Operating Temperature Range	-30℃~+60℃
Protective Class	Class I

Made in China

Manufacturer : Shenzhen SOFAR SOLAR Co.,Ltd.
Address : 401, Building 4, AnTongDa Industrial Park, District 68, XingDong Community,XinAn Street, BaoAn District, Shenzhen, China
VDE0126-1-1,VDE-AR-N4105,G99,IEC61727
IEC62116,UTE C15-712-1,AS4777

SOFAR SOLAR Solar Grid-tied Inverter

Model No:	SOFAR 20KTLX-G3
Max.DC Input Voltage	1100V
Operating MPPT Voltage Range	140~1000V
Max. Input Current	26A/26A
Max. PV Isc	36A/36A
Nominal Grid Voltage	3/N/PE,380/400V
Max.Output Current	3x31.9A
Nominal Grid Frequency	50/60Hz
Nominal Output Power	20000W
Max.Output Power	22000VA
Power Factor	1(adjustable+/-0.8)
Ingress Protection	IP65
Operating Temperature Range	-30℃~+60℃
Protective Class	Class I

Made in China

Manufacturer : Shenzhen SOFAR SOLAR Co.,Ltd.
Address : 401, Building 4, AnTongDa Industrial Park, District 68, XingDong Community,XinAn Street, BaoAn District, Shenzhen, China
VDE0126-1-1,VDE-AR-N4105,G99,IEC61727
IEC62116,UTE C15-712-1,AS4777

SOFAR SOLAR Solar Grid-tied Inverter

Model No:	SOFAR 22KTLX-G3
Max.DC Input Voltage	1100V
Operating MPPT Voltage Range	140~1000V
Max. Input Current	26A/26A
Max. PV Isc	36A/36A
Nominal Grid Voltage	3/N/PE,380/400V
Max.Output Current	3x35.1A
Nominal Grid Frequency	50/60Hz
Nominal Output Power	22000W
Max.Output Power	24200VA
Power Factor	1(adjustable+/-0.8)
Ingress Protection	IP65
Operating Temperature Range	-30℃~+60℃
Protective Class	Class I

Made in China

Manufacturer : Shenzhen SOFAR SOLAR Co.,Ltd.
Address : 401, Building 4, AnTongDa Industrial Park, District 68, XingDong Community,XinAn Street, BaoAn District, Shenzhen, China
VDE0126-1-1,VDE-AR-N4105,G99,IEC61727
IEC62116,UTE C15-712-1,AS4777

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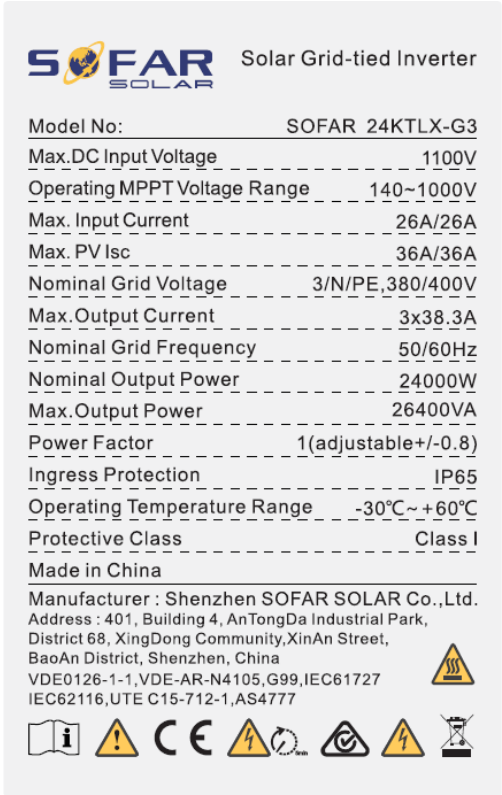

General product information:

The Solar Grid-tied Inverter converts DC voltage into AC voltage.

The input and output are protected by Varistors to Earth. The unit is providing EMC filtering at the output toward mains. The unit does not provide galvanic separation from input to output (transformerless). The output is switched off redundant by the high power switching bridge and a two relays. This assures that the opening of the output circuit will also operate in case of one error.

Description of the electrical circuit:

The internal control is redundant built. It consists of Microcontroller A (U30) and Microcontroller B (U23).

The Microcontroller A (U30) control the relays by switching signals; measures the PV voltage, PV current, Bus voltage, grid voltage, frequency, AC current with injected DC and the array insulation resistance to ground. In addition it tests the current sensors and the RCMU circuit before each start up.

The Microcontroller B (U23) is measures the grid voltage, grid frequency, DCI and residual current, also can switch off the relays independently, and communicate with the Microcontroller A (U30) each other.

The current is measured by a current sensor. The AC current signal and the injected DC current signal are sent to the Microcontroller A (U30). The Microcontroller A (U30) tests and calibrates before each start up all current sensors.

The unit provides two relays in series in all output conductors. When single fault applied to one relay, alarm an error code in display panel, another redundant relay provides basic insulation maintained between the PV array and the mains. All the relays are tested before each start up.

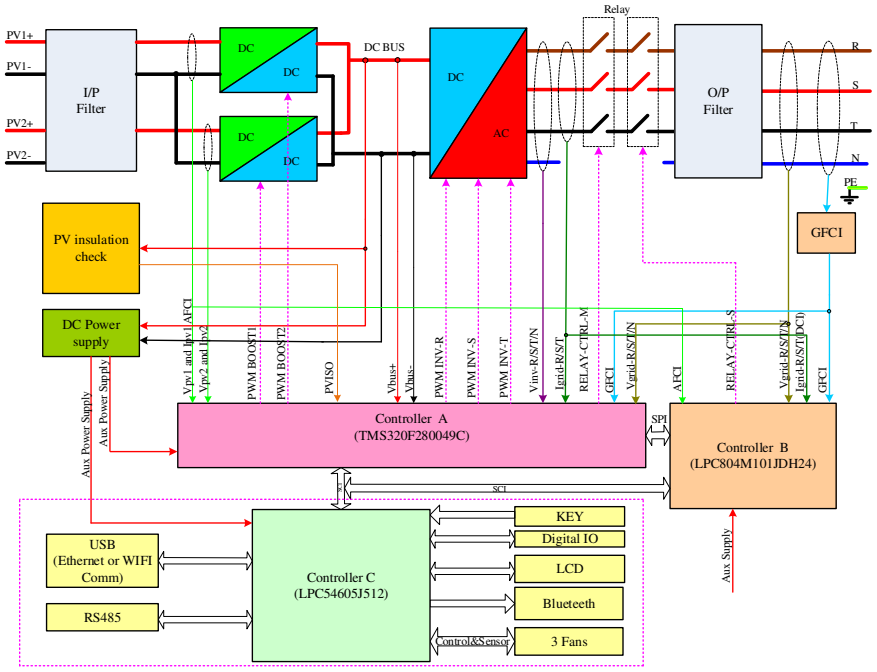

Figure 1 – Block diagram

Model difference:

The models SOFAR 15KTL-G3, SOFAR 17KTL-G3, SOFAR 20KTL-G3, SOFAR 22KTL-G3 and SOFAR 24KTL-G3 are use the identical hardware platform, control unit, control system and software except the output power derated by software and in following table descripts for different.

	SOFAR 15KTLX-G3	SOFAR 17KTLX-G3	SOFAR 20KTLX-G3	SOFAR 22KTLX-G3	SOFAR 24KTLX-G3
Thin-film capacitor of BUS	4pcs (110uF, 550V)	6pcs (110uF, 550V)			
INV IGBT (Q60, Q67, Q71 Q72, Q75, Q76)	6pcs 40A, 1200V	6pcs 75A, 1200V			
Fan	1	2			

The product was tested on:

Hardware Version: V101
Software Version: V010000

All tests were performed on SOFAR 15KTL-G3 and SOFAR 24KTL-G3 are valid for the SOFAR 17KTL-G3, SOFAR 20KTL-G3 and SOFAR 22KTL-G3 since it's use the identical hardware and software construction except output power derated by software.

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Clause	Requirement + Test	Result - Remark	Verdict
4	**Testing circuit**		**P**
	The testing circuit shown in Figure 1 is employed.	Considered.	P
	Similar circuits are used for three-phase output.		P
	Parameters to be measured are shown in Table 1 and Figure 1. Parameters to be recorded in the test report are discussed in Clause 7.	Considered.	P
5	**Testing equipment**		P
5.1	**Measuring instruments**		P
	The waveform measurement/capture device is able to record the waveform from the beginning of the islanding test until the EUT ceases to energize the island.		P
	For multi-phase EUT, all phases are monitored.	Three phases ara monitored.	P
	A waveform monitor designed to detect and calculate the run-on time may be used.	Oscilloscope is used.	P
	For multi-phase EUT, the test and measurement equipment is recorded each phase current and each phase-to-neutral or phase-to-phase voltage, as appropriate, to determine fundamental frequency active and reactive power flow over the duration of the test.	Considered.	P
	A sampling rate of 10 kHz or higher is recommended. The minimum measurement accuracy is 1 % or less of rated EUT nominal output voltage and 1 % or less of rated EUT output current	Considered.	P
	Current, active power, and reactive power measurements through switch S1 used to determine the circuit balance conditions report the fundamental (50 Hz or 60 Hz) component.	Considered.	P
5.2	**DC power source**		P
5.2.1	**General**		**P**
	A PV array or PV array simulator (preferred) may be used. If the EUT can operate in utility-interconnected mode from a storage battery, a DC power source may be used in lieu of a battery as long as the DC power source is not the limiting device as far as the maximum EUT input current is concerned.	PV array simulator is used.	P
	The DC power source provides voltage and current necessary to meet the testing requirements described in Clause 6.	Considered.	P
5.2.2	**PV array simulator**		**P**
	The tests are conducted at the input voltage defined in Table 2 below, and the current is limited to 1,5 times the rated photovoltaic input current, except when specified otherwise by the test requirements.	Considered.	P
	A PV array simulator is recommended, however, any type of power source may be used if it does not influence the test results.	PV array simulator is used.	P

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Clause	Requirement + Test	Result - Remark	Verdict

5.2.3	**Current and voltage limited DC power supply with series resistance**	PV array simulator is used.	**N/A**
	A DC power source used as the EUT input source is capable of EUT maximum input power (so as to achieve EUT maximum output power) at minimum and maximum EUT input operating voltage.		N/A
	The power source provides adjustable current and voltage limit, set to provide the desired short circuit current and open circuit voltage when combined with the series and shunt resistance described below.		N/A
	A series resistance (and, optionally, a shunt resistance) is selected to provide a fill factor within the range: Output power: Sufficient to provide maximum EUT output power and other levels specified by test conditions of table 5. Response speed: The response time of a simulator to a step in output voltage, due to a 5% load change, results in a settling of the output current to within 10% of its final value in less than 1ms. Stability: Excluding the variations caused by the EUT MPPT, simulator output power remains stable within 2 % of specified power level over the duration of the test: from the point where load balance is achieved until the island condition is cleared or the allowable run-on time is exceeded. Power factor: 0.25 to 0.8		N/A
5.2.4	**PV array**	PV array simulator is used.	**N/A**
	A PV array used as the EUT input source is capable of EUT maximum input power at minimum and maximum EUT input operating voltage.		N/A
	Testing is limited to times when the irradiance varies by no more than 2 % over the duration of the test as measured by a silicon-type pyranometer or reference device. It may be necessary to adjust the array configuration to achieve the input voltage and power levels prescribed in 6.1.		N/A
5.3	**AC power source**		**P**
	The utility grid or other AC power source may be used as long as it meets the conditions specified in Table 4. **Table 4 – AC power source requirements** Items: Voltage — Conditions: Nominal ±2,0 % Items: Voltage THD — Conditions: < 2,5 % Items: Frequency — Conditions: Nominal ±0,1 Hz Items: Phase angle distance [1] — Conditions: 120 ° ± 1,5 ° [1] Three-phase case only	Considered.	P
5.4	**AC loads**		**P**

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Clause	Requirement + Test	Result - Remark	Verdict
	On the AC side of the EUT, variable resistance, capacitance, and inductance are connected in parallel as loads between the EUT and the AC power source. Other sources of load, such as electronic loads, may be used if it can be shown that the source does not cause results that are different than would be obtained with passive resistors, inductors, and capacitors.	Considered.	P
	All AC loads are rated for and adjustable to all test conditions. The equations for Qf are based upon an ideal parallel RLC circuit. For this reason, non-inductive resistors, low loss (high Qf) inductors, and capacitors with low effective series resistance and effective series inductance are utilized in the test circuit. Iron core inductors, if used, are not exceed a current THD of 2 % when operated at nominal voltage. Load components are conservatively rated for the voltage and power levels expected. Resistor power ratings are chosen so as to minimize thermally-induced drift in esistance values during the course of the test.	Considered.	P
	Active and reactive power is calculated (using the measurements provided in Table 1) in each of the R, L and C legs of the load so that these parasitic parameters (and parasitics introduced by variacs or autotransformers) are properly accounted for when calculating Qf.	Considered.	P
6	**Test for single or multi-phase inverter**		**P**
6.1	**Test procedure**	(see appended table)	**P**
	The test uses an RLC load, resonant at the EUT nominal frequency (50 Hz or 60 Hz) and matched to the EUT output power.		P
	For multi-phase EUT, the load is balanced across all phases and the switch S1 as in Figure 1 opens all phases	The switch could open all phases.	P
	This test is performed with the EUT conditions as in Table 5, where power and voltage values are given as a percent of EUT full output rating.		P
	a)..Determine EUT test output power		P
	b) .Adjusting the DC input source		P
	c) .Turn off the EUT and open S1		P
	d) .Adjust the RLC circuit to have Qf = 1.0 ±0.05		P
	e)..Connect the RLC load configured in step d) to the EUT by closing S2		P
	f)...Open the utility-disconnect switch S1 to initiate the test, Run-on time is recorded.		P
	g)..For test condition A, adjust the real load and only one of the reactive load components to each of the load imbalance conditions shown in the shaded portion of table 6. If any of the recorded run-on times are longer than the one recorded for the rated balance condition, then the non-shaded parameter combinations also require testing.		P

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Clause	Requirement + Test	Result - Remark	Verdict
	h) For test condition B and C, adjust the only one reactive load components by approximately 1,0% per test, within a total range of 95% to 105% of the operating point. If run-on times are still increasing at the 95% or 105% points, additional 1% increments have to be taken until run-on times begin decreasing.		P
6.2	**Pass/fail criteria**		**P**
	An EUT is considered to comply with the requirements for islanding protection when each case of recorded run-on time is less than 2 s or meets the requirements of local codes.		P
7	**Documentation**		**P**
	At a minimum, the following information is recorded and maintained in the test report.		P
	a) Specifications of EUT. Table 8 provides an example of the type of information that is provided.		P
	b) Measurement results. Table 9 provides an example of the type of information that is provided. Actual measured values is to be recorded.		P
	c) Block diagram of test circuit.		P
	d) Specifications of the test and measurement equipment. Table 10 provides an example of the type of information that is provided.		P
	e) Any test configuration or procedure details such as methods of achieving specified load and EUT output conditions.		P
	f) Any additional information required by the testing laboratory's accreditation.		P
	g) Specify the evaluation criterion from clause 6.2 that was utilized to determine if the product passed or failed the test.		P
Annex A	Islanding as it applies to PV systems(Informative)		--
A.1	General		--
A.2	Impact of distortion on islanding		--
Annex B	Test for independent islanding detection device (relay)(Informative)		--
B.1	Introduction		--
B.2	Testing circuit		--
B.3	Testing equipment		--
B.4	Testing procedure		--
B.5	Documentation		--

IEC 62116			
Clause	Requirement + Test	Result - Remark	Verdict

Test overview:

IEC 62116:2014

Clause	Test	Result
	Type test:	
6.1	Islanding protection according table 6 - Load imbalance (real, reactive load) for test condition A (EUT ouput = 100%)	**P**
6.1	Load imbalance (reactive load) for test condition B (EUT output = 50 % – 66 %)	**P**
6.1	Load imbalance (reactive load) for test condition C (EUT output = 25 % – 33 %)	**P**

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Clause	Requirement + Test	Result - Remark	Verdict

6.1 Islanding protection

Test circuit and parameters

Parameter	Symbol	Units
EUT DC Input		
DC voltage	V_{DC}	V
DC Current	I_{DC}	A
DC Power	P_{DC}	W
EUT AC ouput		
AC voltage	V_{EUT}	V
AC current	I_{EUT}	A
Real power	P_{EUT}	W
Reactive power	Q_{EUT}	VAr
Test Load		
Resistive load current	I_R	A
Inductive load current	I_L	A
Capacitive load current	I_C	A
AC (utility) power source		
Utility real power	P_{AC}	W
Utility reactive power	Q_{AC}	VAr
Utility current	I_{AC}	A

Block diagram test circuit IEC 62116:2008

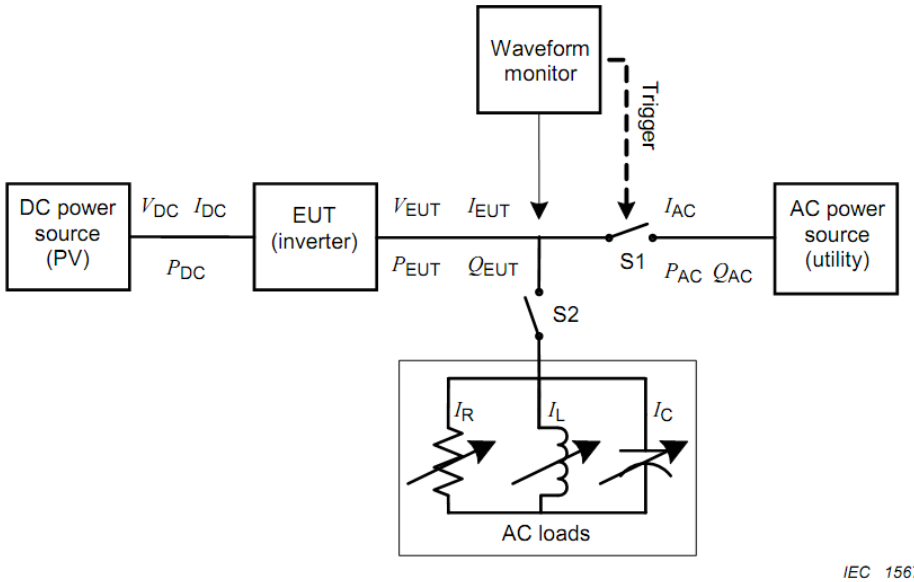

Figure 1 – Test circuit for islanding detection function in a power conditioner (inverter)

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Clause	Requirement + Test	Result - Remark	Verdict

6.1 Islanding protection according table 6 - Load imbalance (real, reactive load) for test condition A (EUT output = 100%)	**P**

SOFAR 24KTLX-G3

Test conditions	Frequency: 50+/-0,1Hz U_N=230+/-3Vac Distortion factor of chokes < 2% Quality = 1
Disconnection limit	2s

No	P_{EUT} 1) [% of EUT rating]	Reactive load [% of Q_L in 6.1.d) 1]	P_{AC} 2) [% of nominal]	Q_{AC} 3) [% of nominal]	I_{AC} 4) [A]	P_{EUT} [kW per phase]	V_{DC} [V]	Q_f [1]	Run on Time [ms]	Remarks 5)
1	100	100	0	0	0,237	8,005	734	1,001	**501**	BL
2	100	100	-5	-5	1,932	8,005	734	1,027	449	IB
3	100	100	-5	0	1,977	8,005	734	1,054	379	IB
4	100	100	-5	+5	1,931	8,005	734	1,080	437	IB
5	100	100	0	-5	0,280	8,005	734	0,976	438	IB
6	100	100	0	+5	0,281	8,005	734	1,026	395	IB
7	100	100	+5	-5	2,018	8,005	734	0,930	397	IB
8	100	100	+5	0	1,977	8,005	734	0,954	469	IB
9	100	100	+5	+5	2,019	8,005	734	0,977	416	IB

Parameter at 0% per phase	L= 21,01 mH	R= 6,61 Ω	C= 482,16 µF

Note:
RLC is adjusted to min. +/-1% of the inverter rated output power
1) P_{EUT}: EUT output power
2) P_{AC}: Real power flow at S1 in Figure 1. Positive means power from EUT to utility. Nominal is the 0 % test condition value.
3) Q_{AC}: Reactive power flow at S1 in Figure 1. Positive means power from EUT to utility. Nominal is the 0 % test condition value.
4) Fundamental of I_{AC} when RLC is adjusted
5) BL: Balance condition, IB: Imbalance condition.
Condition A:
EUT output power PEUT = Maximum 6)
EUT input voltage 6) = >75% of rated input voltage range
6) Maximum EUT output power condition should be achieved using the maximum allowable input power. Actual output power may exceed nominal rated output.
7) Based on EUT rated input operating range. For example, If range is between X volts and Y volts, 90 % of range =X + 0,75 × (Y – X). Y shall not exceed 0,8 × EUT maximum system voltage (i.e., maximum allowable array open circuit voltage). In any case, the EUT should not be operated outside of its allowable input voltage range.
The tests had been performed on the SOFAR 24KTLX-G3 and SOFAR 15KTLX-G3 is valid for the SOFAR 17KTLX-G3, SOFAR 20KTLX-G3 and SOFAR 22KTLX-G3, since it is identical in hardware and software construction except output power derated by software.

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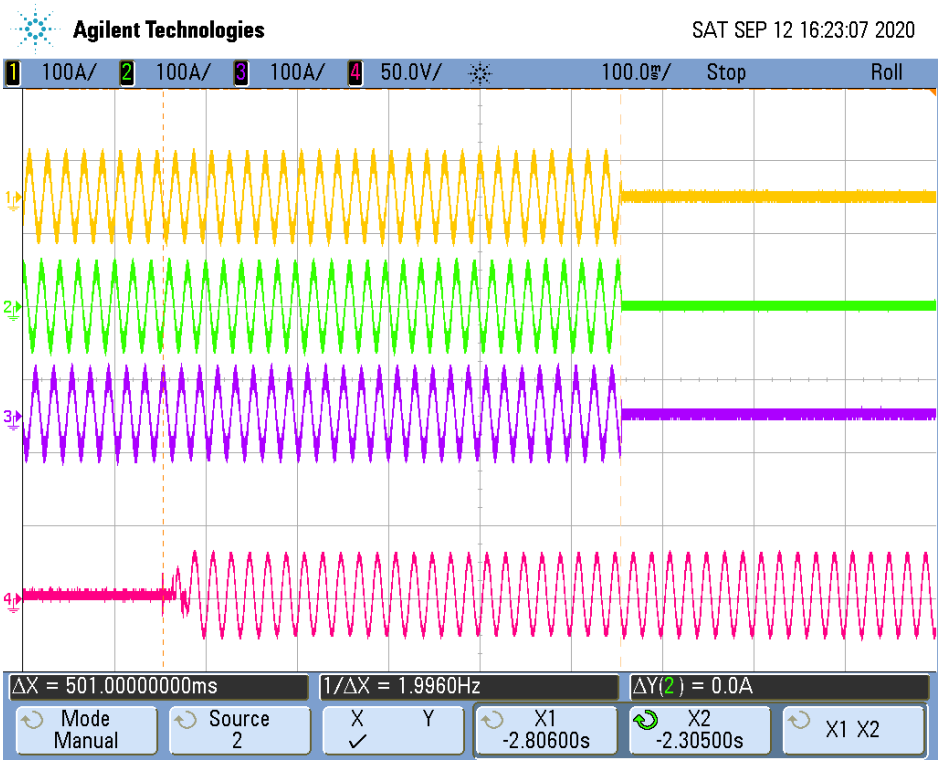
Clause	Requirement + Test	Result - Remark	Verdict

Disconnection at P_{AC} 0% and Q_{AC} 0% reactive load and 100% nominal power



IEC 62116			
Clause	Requirement + Test	Result - Remark	Verdict

6.1 Islanding protection according Table 7 – Load imbalance (reactive load) for test condition B (EUT output = 50 % – 66 %) — **P**

SOFAR 24KTLX-G3

Test conditions	Frequency: 50+/-0,1Hz U_N=230+/-3Vac Distortion factor of chokes < 2% Quality =1
Disconnection limit	2s

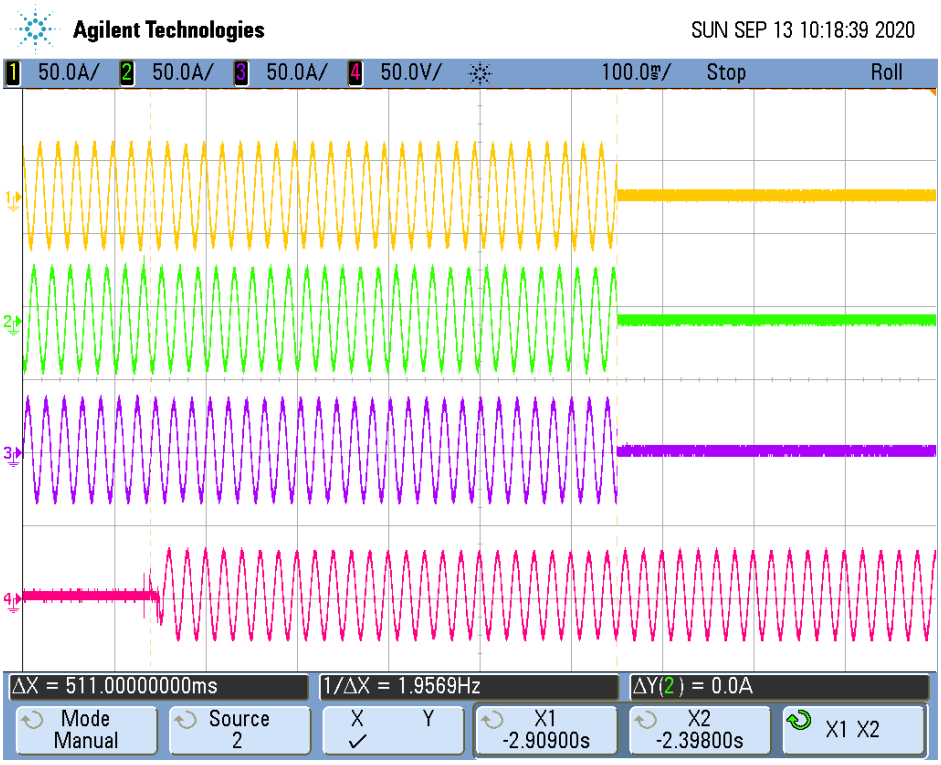
No	P_{EUT} 1) [% of EUT rating]	Reactive load [% of Q_L in 6.1.d) 1]	P_{AC} 2) [% of nominal]	Q_{AC} 3) [% of nominal]	I_{AC} 4) [A]	P_{EUT} [kW per phase]	V_{DC} [V]	Q_f [1]	Run on Time [ms]	Remarks 5)
12	66	66	0	-5	0,226	5,036	470	0,974	413	IB
13	66	66	0	-4	0,216	5,036	470	0,979	481	IB
14	66	66	0	-3	0,208	5,036	470	0,985	469	IB
15	66	66	0	-2	0,202	5,036	470	0,990	463	IB
16	66	66	0	-1	0,198	5,036	470	0,995	384	IB
2	66	66	0	0	0,197	5,036	470	1,000	**511**	BL
17	66	66	0	1	0,198	5,036	470	1,005	480	IB
18	66	66	0	2	0,201	5,036	470	1,010	500	IB
19	66	66	0	3	0,207	5,036	470	1,015	484	IB
20	66	66	0	4	0,215	5,036	470	1,019	489	IB
21	66	66	0	5	0,225	5,036	470	1,024	474	IB

Parameter at 0% per phase	L= 31,74 mH	R= 9,97 Ω	C= 319,27 µF

Note:
RLC is adjusted to min. +/-1% of the inverter rated output power
1) P_{EUT}: EUT output power
2) P_{AC}: Real power flow at S1 in Figure 1. Positive means power from EUT to utility. Nominal is the 0 % test condition value.
3) Q_{AC}: Reactive power flow at S1 in Figure 1. Positive means power from EUT to utility. Nominal is the 0 % test condition value.
4) Fundamental of I_{AC} when RLC is adjusted
5) BL: Balance condition, IB: Imbalance condition.
Condition B:
EUT output power P_{EUT} = 50 % – 66 % of maximum
EUT input voltage 6) = 50 % of rated input voltage range, ±10 %
6) Based on EUT rated input operating range. For example, If range is between X volts and Y volts, 50 % of range =X + 0,5 × (Y – X). Y shall not exceed 0,8 × EUT maximum system voltage (i.e., maximum allowable array open circuit voltage). In any case, the EUT should not be operated outside of its allowable input voltage range.

IEC 62116			
Clause	Requirement + Test	Result - Remark	Verdict

Disconnection at P_{AC} 0% and Q_{AC} 0% reactive load and 66% nominal power



IEC 62116			
Clause	Requirement + Test	Result - Remark	Verdict

6.1 Islanding protection according Table 7 – Load imbalance (reactive load) for test condition C (EUT output = 25 % – 33 %)	**P**

SOFAR 24KTLX-G3

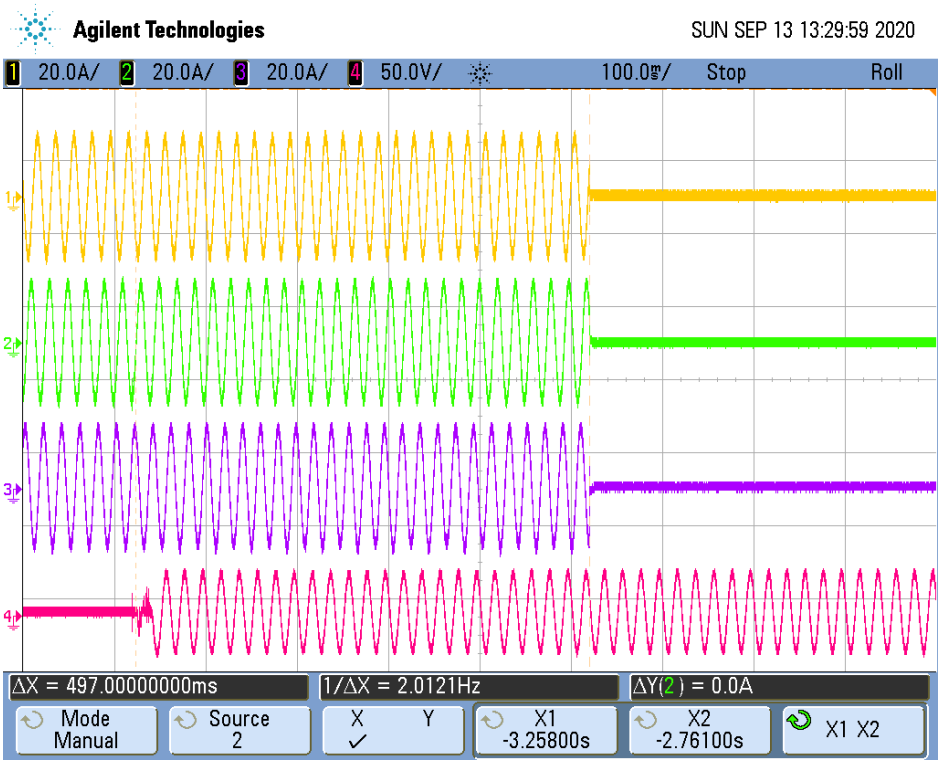
Test conditions			Frequency: 50+/-0,1Hz U_N=230+/-3Vac Distortion factor of chokes < 2% Quality =1							
Disconnection limit			2s							
No	P_{EUT} [1] [% of EUT rating]	Reactive load [% of Q_L in 6.1.d) 1]	P_{AC} [2] [% of nominal]	Q_{AC} [3] [% of nominal]	I_{AC} [4] [A]	P_{EUT} [kW per phase]	V_{DC} [V]	Q_f [1]	Run on Time [ms]	Remarks [5]
22	33	33	0	-5	0,128	2,635	206	0,977	411	IB
23	33	33	0	-4	0,123	2,635	206	0,982	487	IB
24	33	33	0	-3	0,119	2,635	206	0,987	368	IB
25	33	33	0	-2	0,116	2,635	206	0,992	418	IB
26	33	33	0	-1	0,114	2,635	206	0,997	410	IB
3	33	33	0	0	0,113	2,635	206	1,002	**497**	BL
27	33	33	0	1	0,113	2,635	206	1,007	372	IB
28	33	33	0	2	0,115	2,635	206	1,012	439	IB
29	33	33	0	3	0,118	2,635	206	1,017	348	IB
30	33	33	0	4	0,122	2,635	206	1,022	455	IB
31	33	33	0	5	0,127	2,635	206	1,027	414	IB

Parameter at 0% per phase	L= 63,78 mH	R= 20,08 Ω	C= 158,87 µF

Note:
RLC is adjusted to min. +/-1% of the inverter rated output power
[1] P_{EUT}: EUT output power
[2] P_{AC}: Real power flow at S1 in Figure 1. Positive means power from EUT to utility. Nominal is the 0 % test condition value.
[3] Q_{AC}: Reactive power flow at S1 in Figure 1. Positive means power from EUT to utility. Nominal is the 0 % test condition value.
[4] Fundamental of I_{AC} when RLC is adjusted
[5] BL: Balance condition, IB: Imbalance condition.
Condition B:
EUT output power PEUT = 25 % – 33 % [6] of maximum
EUT input voltage [7] = <20 % of rated input voltage range
[6] Or minimum allowable EUT output level if greater than 33 %.
[7] Based on EUT rated input operating range. For example, If range is between X volts and Y volts, 10 % of range =X + 0,2 × (Y – X). Y shall not exceed 0,8 × EUT maximum system voltage (i.e., maximum allowable array open circuit voltage). In any case, the EUT should not be operated outside of its allowable input voltage range.

IEC 62116			
Clause	Requirement + Test	Result - Remark	Verdict

Disconnection at P_{AC} 0% and Q_{AC} 0% reactive load and 33% nominal power



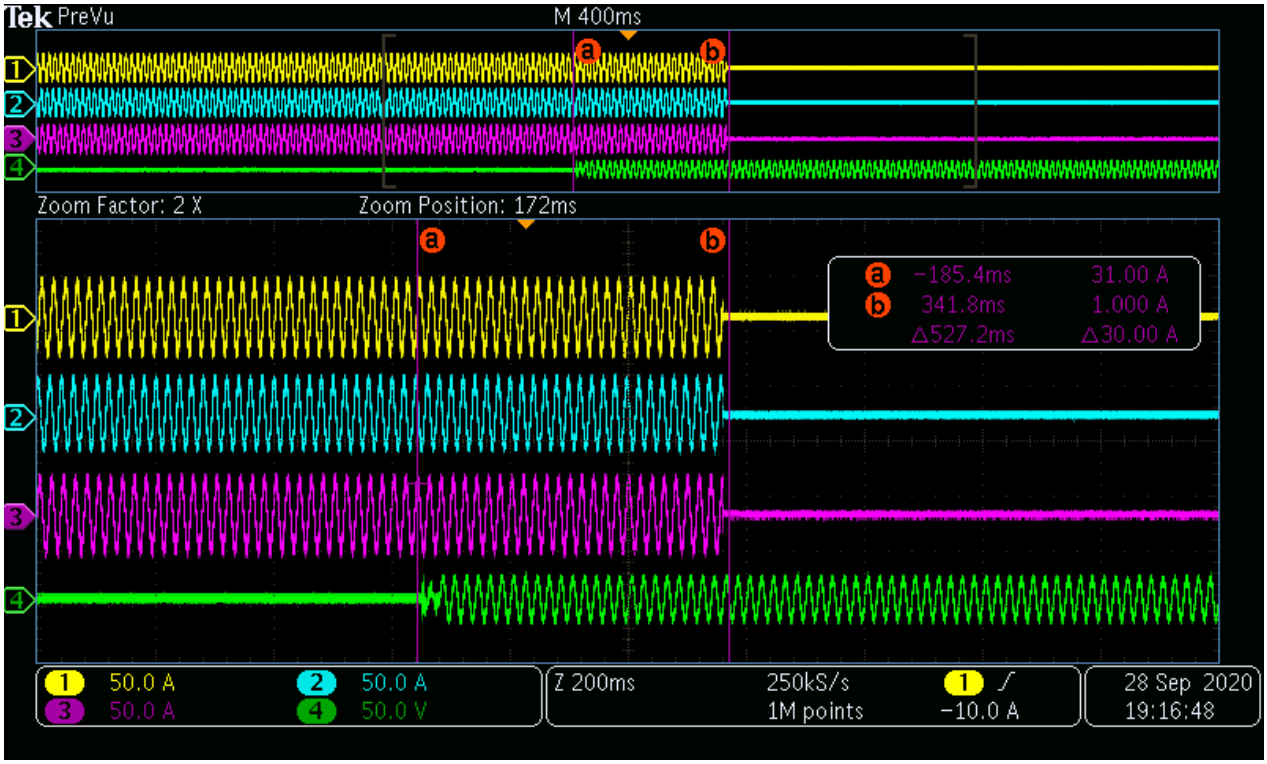
IEC 62116			
Clause	Requirement + Test	Result - Remark	Verdict

6.1 Islanding protection according table 6 - Load imbalance (real, reactive load) for test condition A (EUT output = 100%)										**P**
SOFAR 15KTLX-G3										
Test conditions			Frequency: 50+/-0,1Hz U_N=230+/-3Vac Distortion factor of chokes < 2% Quality = 1							
Disconnection limit			2s							
No	P_{EUT} [1] [% of EUT rating]	Reactive load [% of Q_L in 6.1.d) 1]	P_{AC} [2] [% of nominal]	Q_{AC} [3] [% of nominal]	I_{AC} [4] [A]	P_{EUT} [kW per phase]	V_{DC} [V]	Q_f [1]	Run on Time [ms]	Remarks [5]
1	100	100	0	0	0,177	4,989	734	1,001	**527**	BL
2	100	100	-5	-5	1,235	4,989	734	1,027	427	IB
3	100	100	-5	0	1,262	4,989	734	1,054	469	IB
4	100	100	-5	+5	1,231	4,989	734	1,080	377	IB
5	100	100	0	-5	0,202	4,989	734	0,976	409	IB
6	100	100	0	+5	0,206	4,989	734	1,026	423	IB
7	100	100	+5	-5	1,286	4,989	734	0,929	391	IB
8	100	100	+5	0	1,262	4,989	734	0,953	411	IB
9	100	100	+5	+5	1,289	4,989	734	0,977	375	IB
Parameter at 0% per phase			L= 33,72 mH			R= 10,16 Ω			C= 300,05 µF	

Note:
RLC is adjusted to min. +/-1% of the inverter rated output power
[1] P_{EUT}: EUT output power
[2] P_{AC}: Real power flow at S1 in Figure 1. Positive means power from EUT to utility. Nominal is the 0 % test condition value.
[3] Q_{AC}: Reactive power flow at S1 in Figure 1. Positive means power from EUT to utility. Nominal is the 0 % test condition value.
[4] Fundamental of I_{AC} when RLC is adjusted
[5] BL: Balance condition, IB: Imbalance condition.
Condition A:
EUT output power PEUT = Maximum [6]
EUT input voltage [6] = >75% of rated input voltage range
[6] Maximum EUT output power condition should be achieved using the maximum allowable input power. Actual output power may exceed nominal rated output.
[7] Based on EUT rated input operating range. For example, If range is between X volts and Y volts, 90 % of range =X + 0,75 × (Y – X). Y shall not exceed 0,8 × EUT maximum system voltage (i.e., maximum allowable array open circuit voltage). In any case, the EUT should not be operated outside of its allowable input voltage range.
The tests had been performed on the SOFAR 24KTLX-G3 and SOFAR 15KTLX-G3 is valid for the SOFAR 17KTLX-G3, SOFAR 20KTLX-G3 and SOFAR 22KTLX-G3, since it is identical in hardware and software construction except output power derated by software.

IEC 62116			
Clause	Requirement + Test	Result - Remark	Verdict

Disconnection at P_{AC} 0% and Q_{AC} 0% reactive load and 100% nominal power



IEC 62116			
Clause	Requirement + Test	Result - Remark	Verdict

6.1 Islanding protection according Table 7 – Load imbalance (reactive load) for test condition B (EUT output = 50 % – 66 %) — **P**

SOFAR 15KTLX-G3

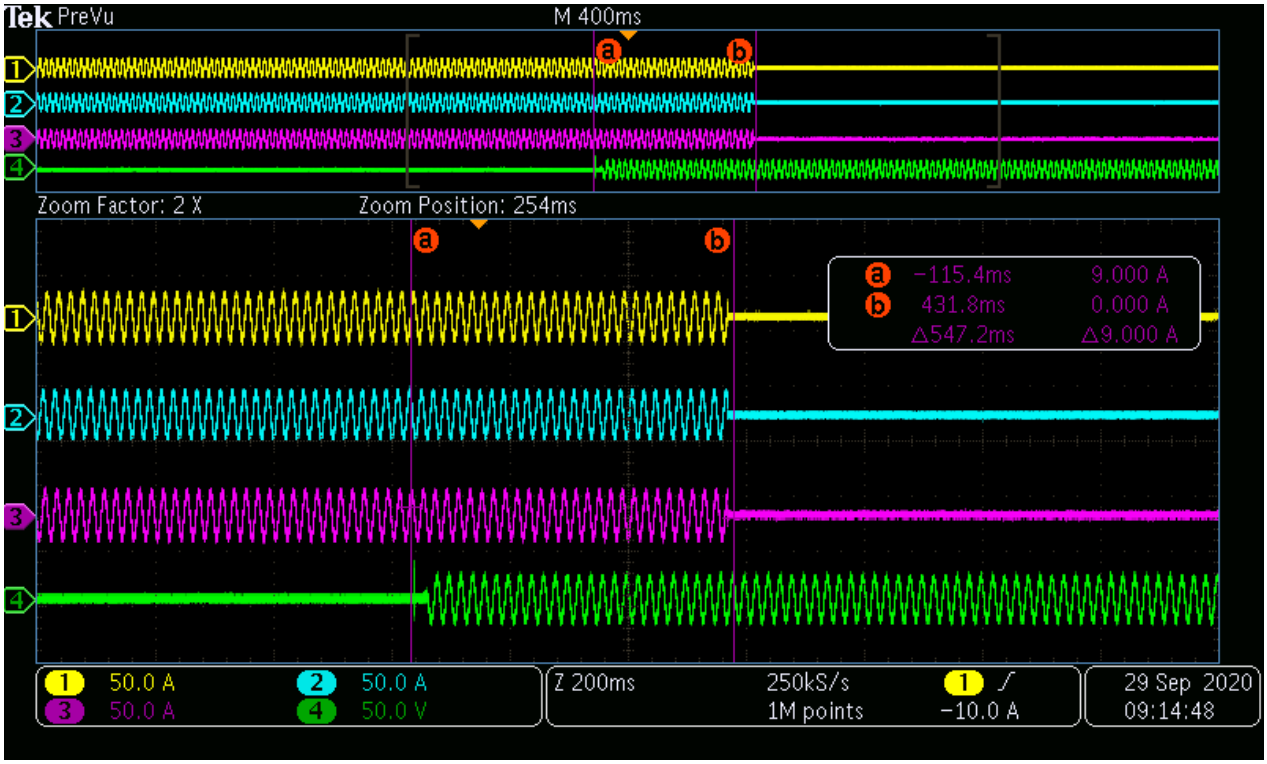
Test conditions			Frequency: 50+/-0,1Hz U_N=230+/-3Vac Distortion factor of chokes < 2% Quality =1							
Disconnection limit			2s							
No	P_{EUT} 1) [% of EUT rating]	Reactive load [% of Q_L in 6.1.d) 1]	P_{AC} 2) [% of nominal]	Q_{AC} 3) [% of nominal]	I_{AC} 4) [A]	P_{EUT} [kW per phase]	V_{DC} [V]	Q_f [1]	Run on Time [ms]	Remarks 5)
12	66	66	0	-5	0,132	3,298	470	0,977	387	IB
13	66	66	0	-4	0,126	3,298	470	0,982	421	IB
14	66	66	0	-3	0,122	3,298	470	0,987	415	IB
15	66	66	0	-2	0,119	3,298	470	0,992	461	IB
16	66	66	0	-1	0,117	3,298	470	0,997	457	IB
2	66	66	0	0	0,117	3,298	470	1,002	**547**	BL
17	66	66	0	1	0,119	3,298	470	1,007	539	IB
18	66	66	0	2	0,121	3,298	470	1,012	391	IB
19	66	66	0	3	0,126	3,298	470	1,017	405	IB
20	66	66	0	4	0,131	3,298	470	1,022	451	IB
21	66	66	0	5	0,138	3,298	470	1,027	415	IB

Parameter at 0% per phase	L= 50,96 mH	R= 16,04 Ω	C= 198,84 µF

Note:
RLC is adjusted to min. +/-1% of the inverter rated output power
1) P_{EUT}: EUT output power
2) P_{AC}: Real power flow at S1 in Figure 1. Positive means power from EUT to utility. Nominal is the 0 % test condition value.
3) Q_{AC}: Reactive power flow at S1 in Figure 1. Positive means power from EUT to utility. Nominal is the 0 % test condition value.
4) Fundamental of I_{AC} when RLC is adjusted
5) BL: Balance condition, IB: Imbalance condition.
Condition B:
EUT output power P_{EUT} = 50 % – 66 % of maximum
EUT input voltage 6) = 50 % of rated input voltage range, ±10 %
6) Based on EUT rated input operating range. For example, If range is between X volts and Y volts, 50 % of range =X + 0,5 × (Y – X). Y shall not exceed 0,8 × EUT maximum system voltage (i.e., maximum allowable array open circuit voltage). In any case, the EUT should not be operated outside of its allowable input voltage range.
The tests had been performed on the SOFAR 24KTLX-G3 and SOFAR 15KTLX-G3 is valid for the SOFAR 17KTLX-G3, SOFAR 20KTLX-G3 and SOFAR 22KTLX-G3, since it is identical in hardware and software construction except output power derated by software.

IEC 62116			
Clause	Requirement + Test	Result - Remark	Verdict

Disconnection at P_{AC} 0% and Q_{AC} 0% reactive load and 66% nominal power



IEC 62116			
Clause	Requirement + Test	Result - Remark	Verdict

6.1 Islanding protection according Table 7 – Load imbalance (reactive load) for test condition C (EUT output = 25 % – 33 %) **P**

SOFAR 15KTLX-G3

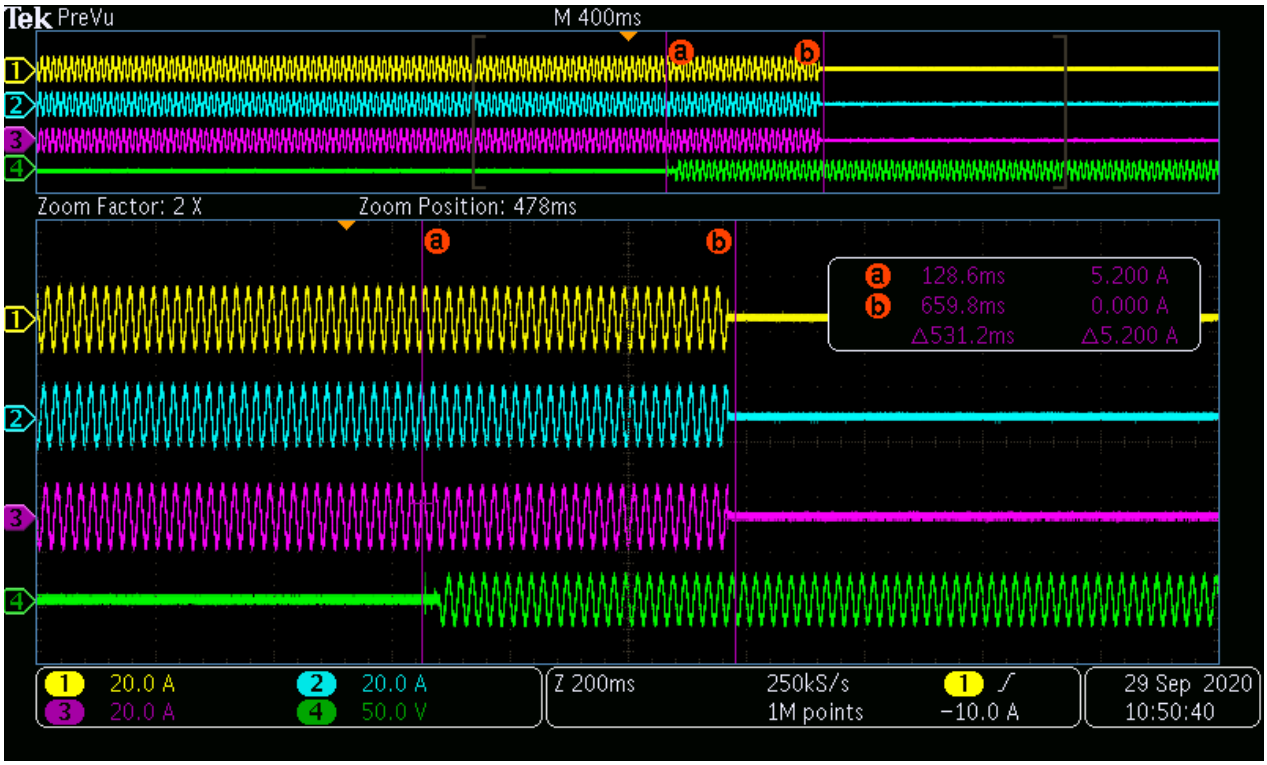
Test conditions			Frequency: 50+/-0,1Hz U_N=230+/-3Vac Distortion factor of chokes < 2% Quality =1							
Disconnection limit			2s							
No	P_{EUT} [1] [% of EUT rating]	Reactive load [% of Q_L in 6.1.d) 1]	P_{AC} [2] [% of nominal]	Q_{AC} [3] [% of nominal]	I_{AC} [4] [A]	P_{EUT} [kW per phase]	V_{DC} [V]	Q_f [1]	Run on Time [ms]	Remarks [5]
22	33	33	0	-5	0,142	1,648	206	0,975	404	IB
23	33	33	0	-4	0,138	1,648	206	0,980	433	IB
24	33	33	0	-3	0,136	1,648	206	0,985	485	IB
25	33	33	0	-2	0,134	1,648	206	0,991	443	IB
26	33	33	0	-1	0,133	1,648	206	0,996	485	IB
3	33	33	0	0	0,133	1,648	206	1,001	**531**	BL
27	33	33	0	1	0,133	1,648	206	1,006	481	IB
28	33	33	0	2	0,135	1,648	206	1,011	421	IB
29	33	33	0	3	0,136	1,648	206	1,016	405	IB
30	33	33	0	4	0,139	1,648	206	1,020	427	IB
31	33	33	0	5	0,142	1,648	206	1,025	379	IB

Parameter at 0% per phase	L= 102,07 mH	R= 32,10 Ω	C= 99,26 µF

Note:
RLC is adjusted to min. +/-1% of the inverter rated output power
[1] P_{EUT}: EUT output power
[2] P_{AC}: Real power flow at S1 in Figure 1. Positive means power from EUT to utility. Nominal is the 0 % test condition value.
[3] Q_{AC}: Reactive power flow at S1 in Figure 1. Positive means power from EUT to utility. Nominal is the 0 % test condition value.
[4] Fundamental of I_{AC} when RLC is adjusted
[5] BL: Balance condition, IB: Imbalance condition.
Condition B:
EUT output power PEUT = 25 % – 33 % [6] of maximum
EUT input voltage [7] = <20 % of rated input voltage range
[6] Or minimum allowable EUT output level if greater than 33 %.
[7] Based on EUT rated input operating range. For example, If range is between X volts and Y volts, 10 % of range =X + 0,2 × (Y – X). Y shall not exceed 0,8 × EUT maximum system voltage (i.e., maximum allowable array open circuit voltage). In any case, the EUT should not be operated outside of its allowable input voltage range.
The tests had been performed on the SOFAR 24KTLX-G3 and SOFAR 15KTLX-G3 is valid for the SOFAR 17KTLX-G3, SOFAR 20KTLX-G3 and SOFAR 22KTLX-G3, since it is identical in hardware and software construction except output power derated by software.

IEC 62116			
Clause	Requirement + Test	Result - Remark	Verdict

Disconnection at P_{AC} 0% and Q_{AC} 0% reactive load and 33% nominal power



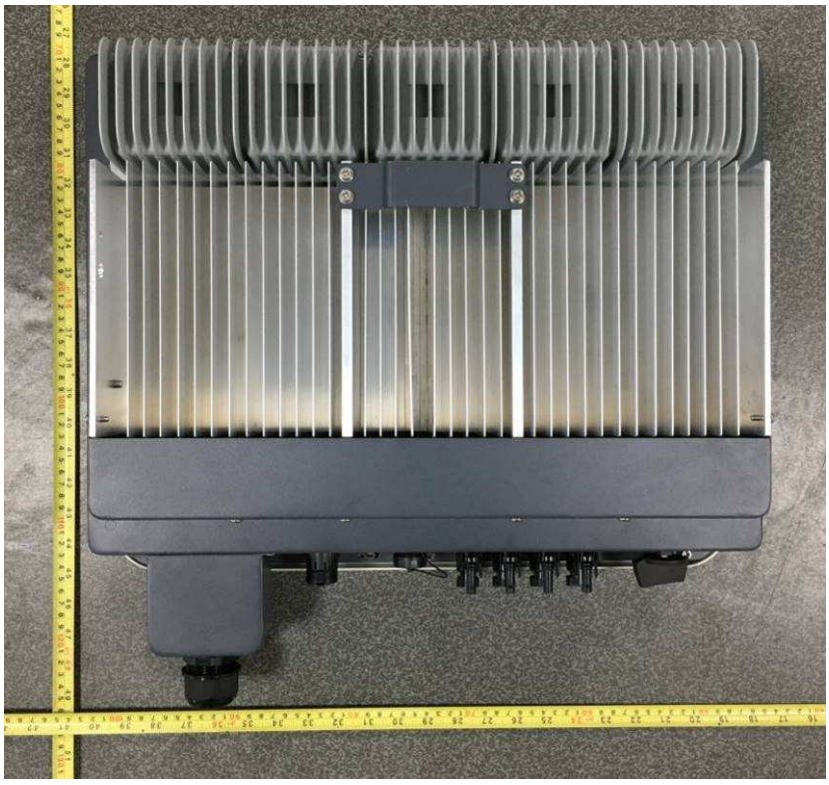
Annex 1

Pictures of the unit

EUT Photo

General view – 1 of Front

General view – 1 of Rear



General view – 1 of Bottom

SOFAR 15KTLX-G3, SOFAR 17KTLX-G3

EUT Photo

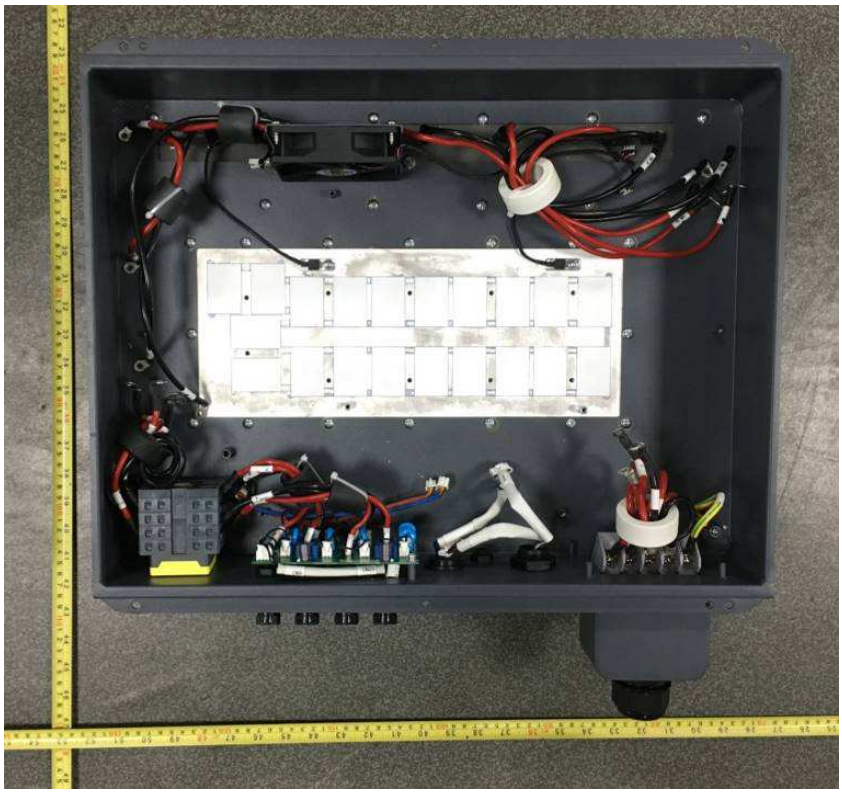
General view – 1 of Bottom

SOFAR 20KTLX-G3, SOFAR 22KTLX-G3, SOFAR 24KTLX-G3

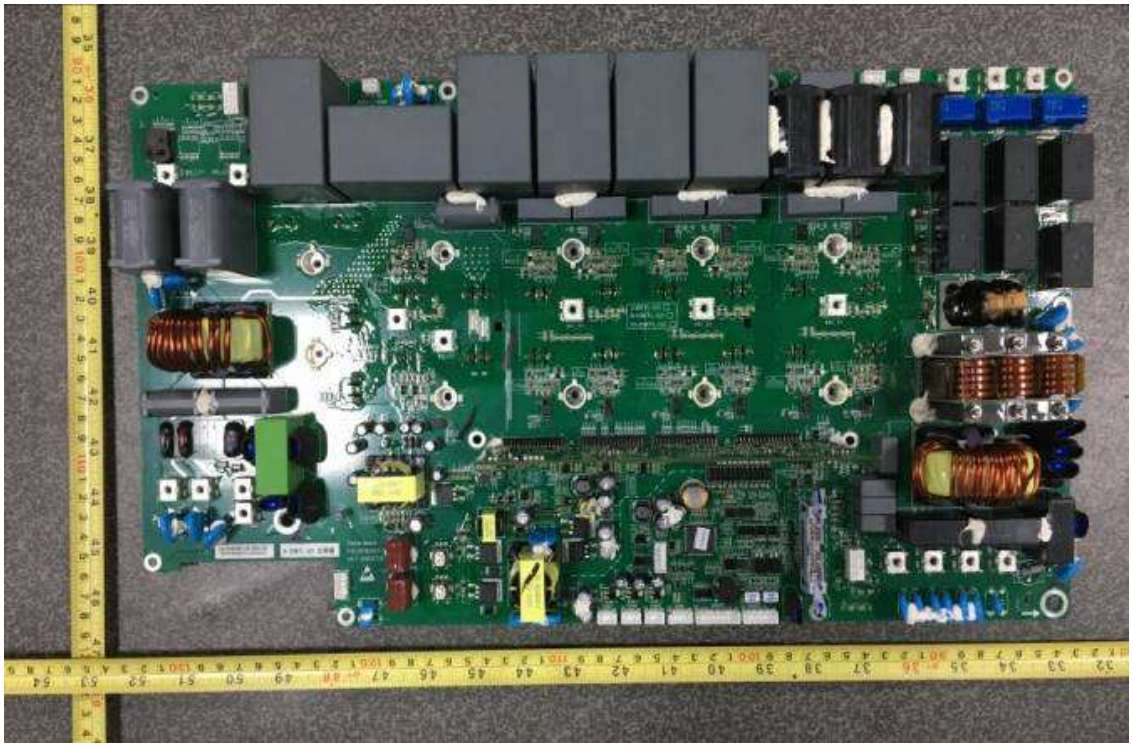
General view – 1 of Side

Internal view – 1

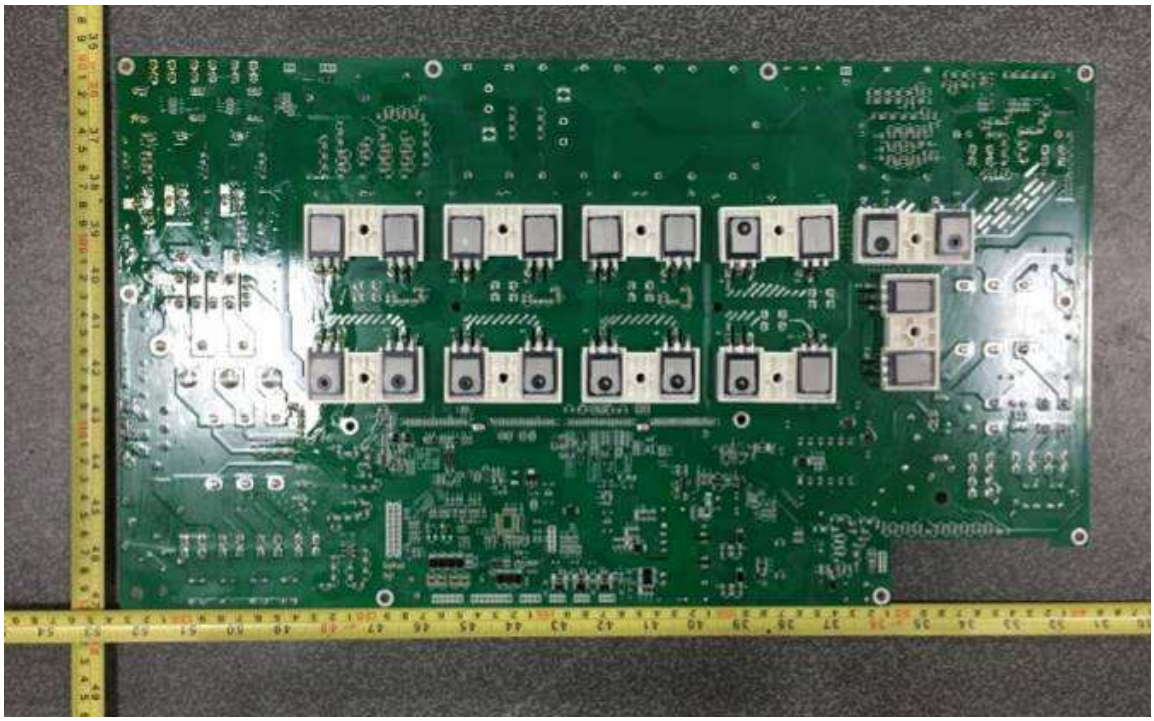
Internal view – 2



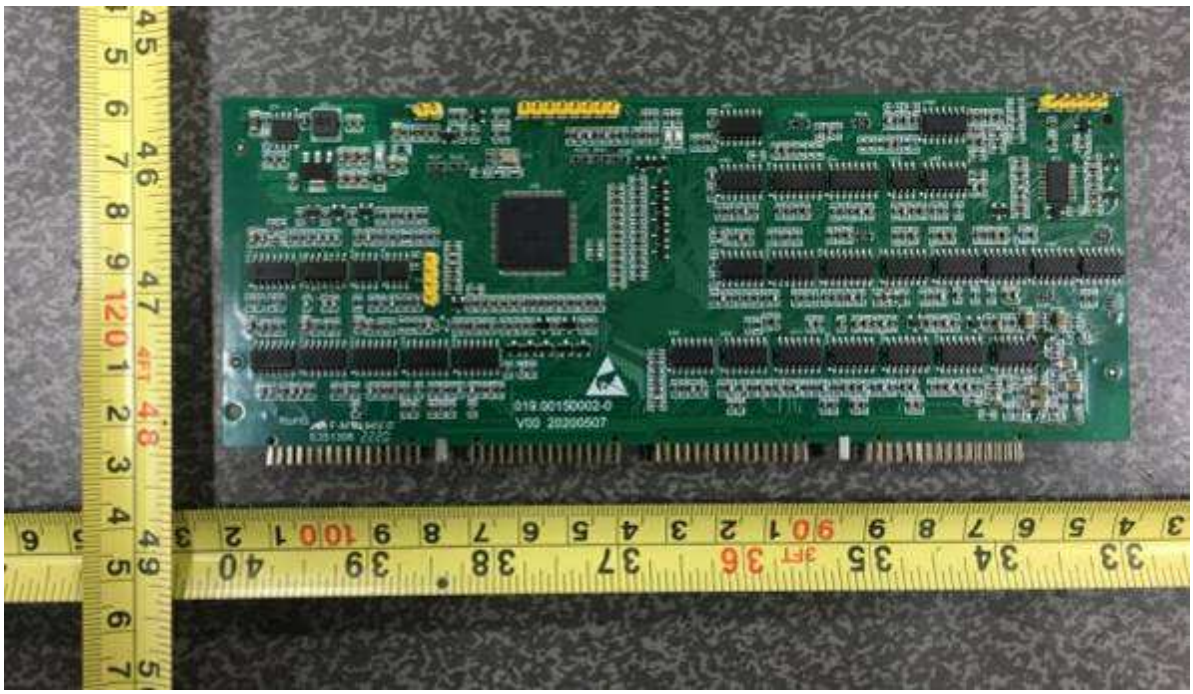
General view – 1 of Power board



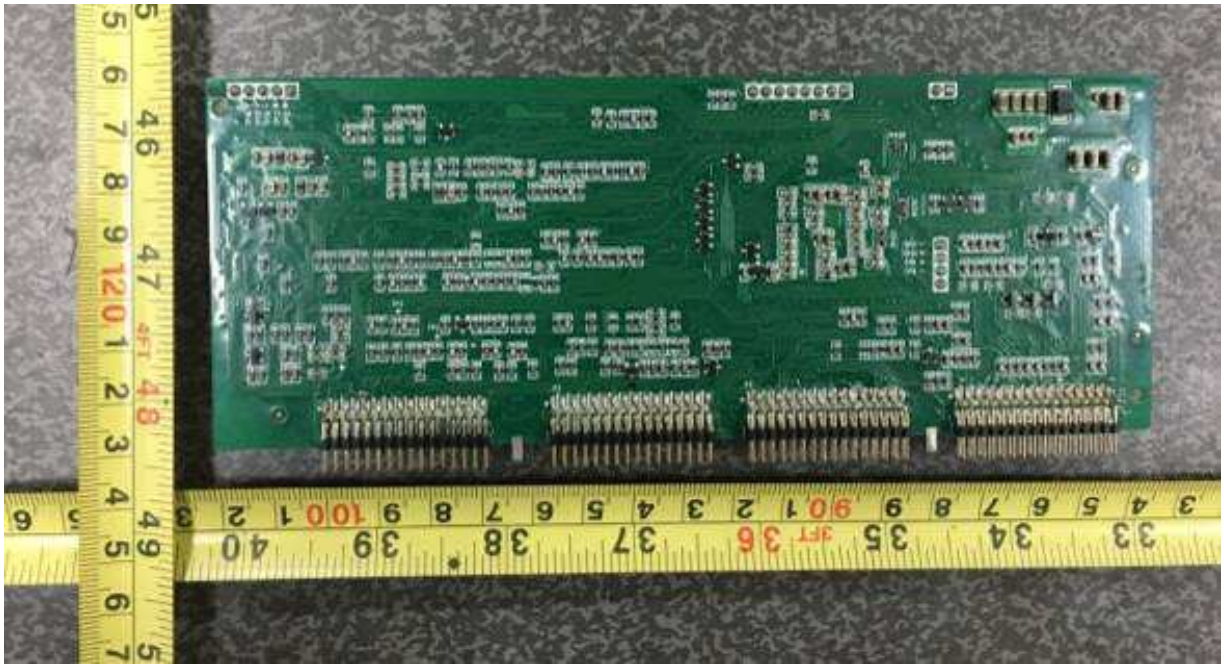
General view – 2 of Power board



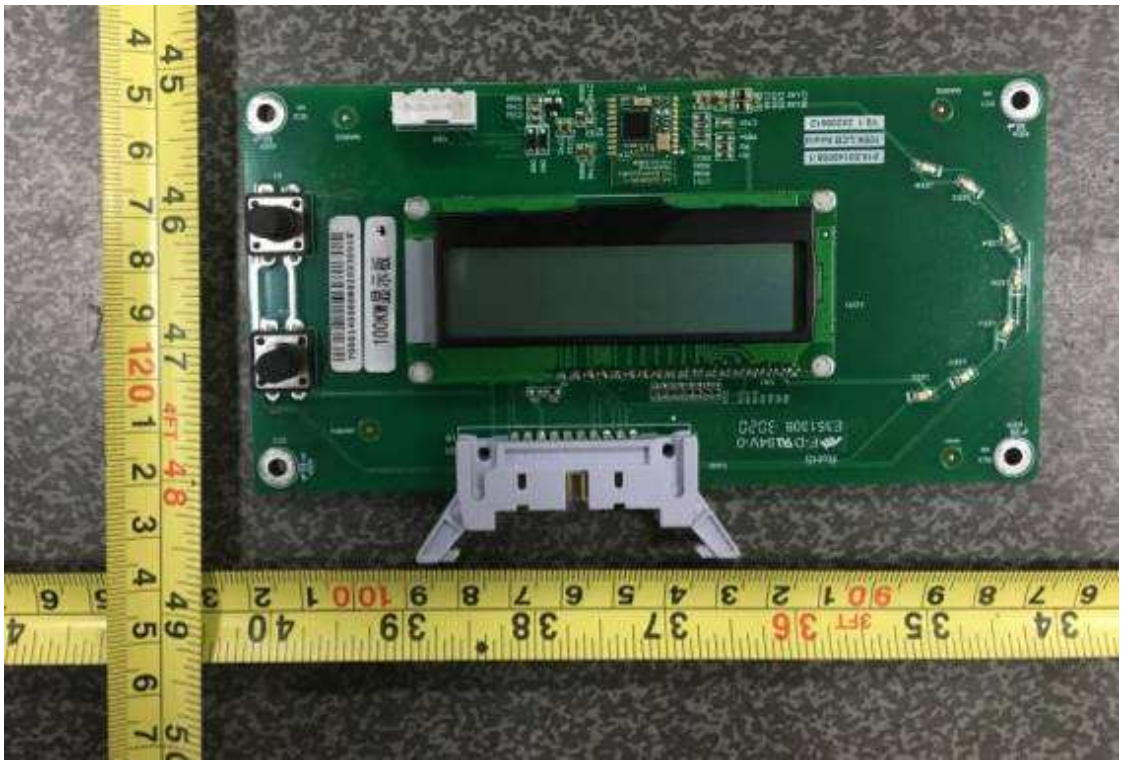
General view – 1 of Control board



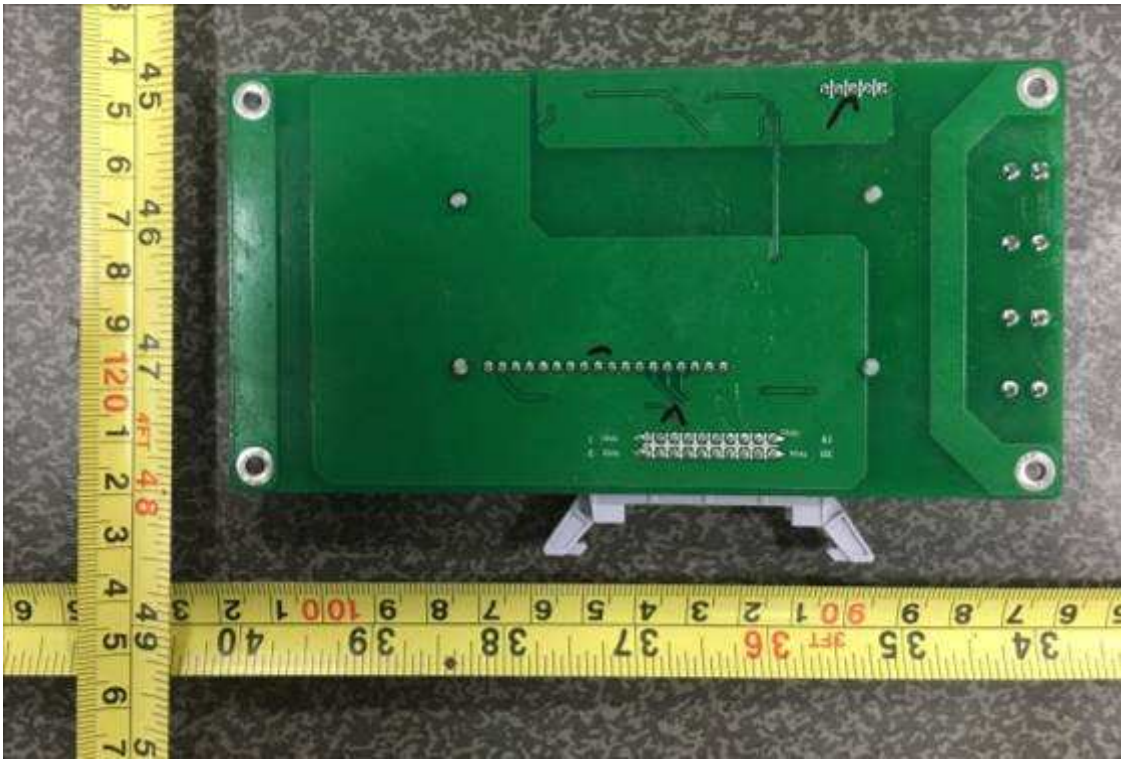
General view – 2 of Control board



General view – 1 of LCD panel



General view – 2 of LCD panel



General view of Grouding point

Annex 2

Test equipment list

Date(s) of performance test: 2020-05-11 to 2020-09-28

Equipment	Internal No.	Manufacturer	Type	Serial No.	Next Calibration date
Power Analyser	A4080002DG	YOKOGAWA	WT3000	91M210852	Jun. 16, 2021
AC Source	A7040019DG	Chroma	61512	61512000439	Monitored by Power Analyser
	A7040020DG	Chroma	61512	61512000438	
DC Simulation Power Supply	A7040015DG	Chroma	62150H-1000S	62150EF00488	
	A7040016DG	Chroma	62150H-1000S	62150EF00490	
	A7040017DG	Chroma	620028	620028EF00120	
RLC Load	A7150027DG	Qunling	ACLT-3803H	93VOO2869	
Oscilloscope probe	A4089008DG	Tektronix	TPP1000	C008230	Aug. 10, 2021
	A4089010DG	Tektronix	TPP1000	C008228	Aug. 10, 2021
	A4089011DG	Tektronix	TPP1000	C008229	Aug. 10, 2021
Current transducer	A1060007DG	YOKOGAWA	CT200	1130700012	Sep. 02, 2021
	A1060008DG	YOKOGAWA	CT200	1130700017	Sep. 02, 2021
	A1060012DG	YOKOGAWA	CT200	1130700018	Sep. 02, 2021
Oscilloscope	//	Agilent	DS05014A	MY50070288	Jan. 13, 2021
Oscilloscope current probe	//	CYBERTEK	CP1000A	C181000922	Jan. 13, 2021
	//	CYBERTEK	CP1000A	C181000925	Jan. 13, 2021
	//	CYBERTEK	CP1000A	C181000929	Jan. 13, 2021
	//	CYBERTEK	CP1000A	C181000931	Jan. 13, 2021
Oscilloscope probe	//	SANHUA	SI-9110	152627	Jan. 13, 2021
	//	SIALENT	DS5034X	SDS5XEAC3R0011	Jan. 13, 2021
	//	AGILENT	N2863B	YF0139	Jan. 13, 2021